# DC to AC Conversion (INVERTER)

- General concept
- Basic principles/concepts
- Single-phase inverter
    - Square wave
    - Notching
    - PWM
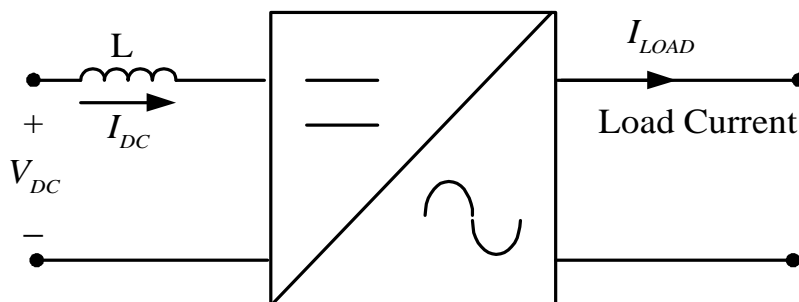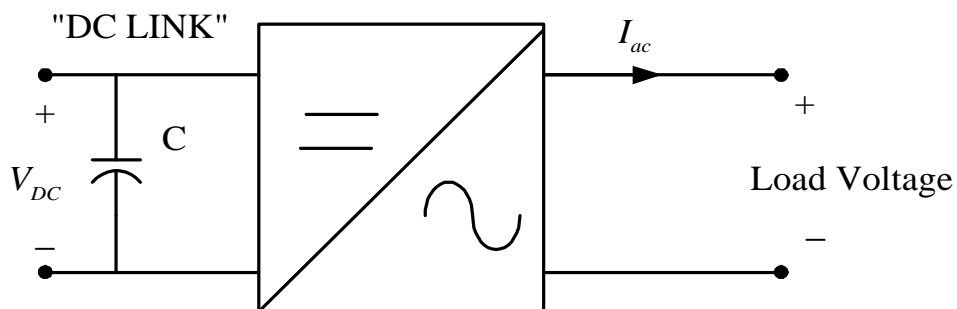- Harmonics
- Modulation
- Three-phase inverter

DC to AC Converter (Inverter)

- DEFINITION: Converts DC to AC power by switching the *DC input voltage (or current)* in a pre-determined sequence so as to generate *AC voltage (or current) output.*

- TYPICAL APPLICATIONS:
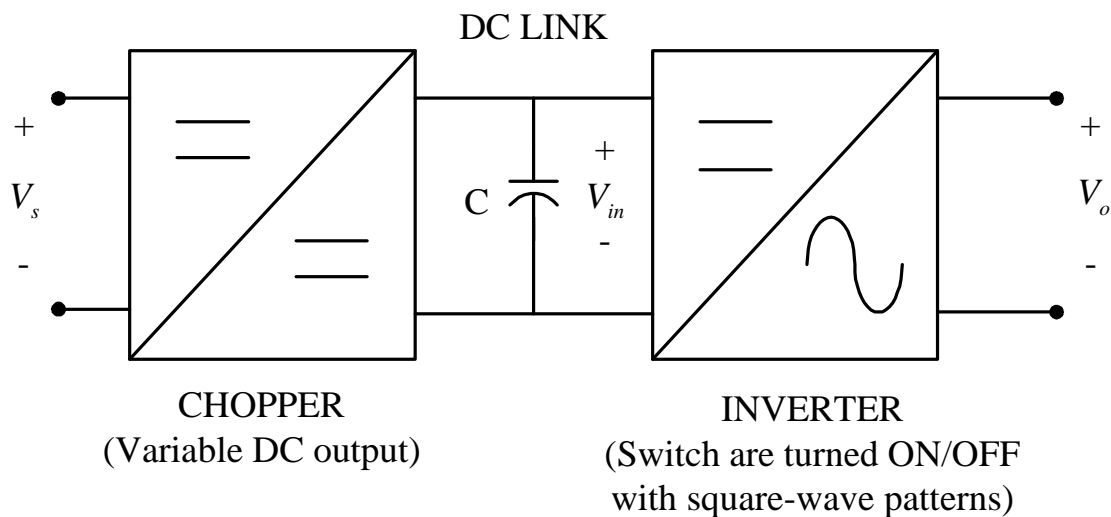 - UPS, Industrial drives, Traction, HVDC

- General block diagram
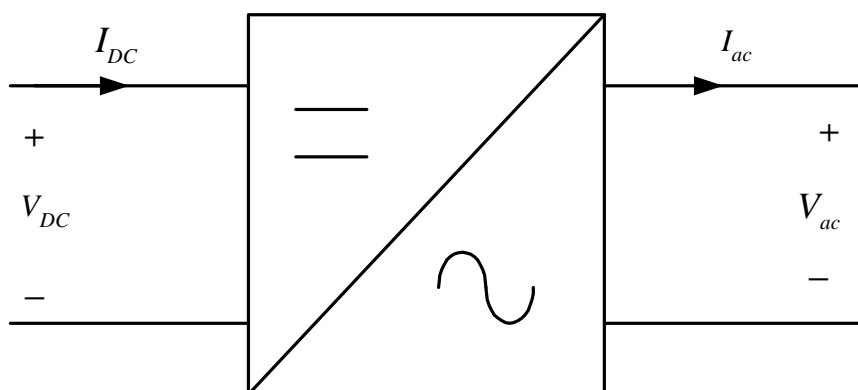
Types of inverter

- Voltage Source Inverter (VSI)
- Current Source Inverter (CSI)

"DC LINK" I_{ac}

$+$ $+$

C

V_{DC} Load Voltage

$-$ $-$

L I_{LOAD}

$+$ I_{DC} Load Current

V_{DC}

$-$

Voltage source inverter (VSI) with variable DC link

DC LINK

$+$

V_s

$-$

C

$+$

V_{in}

$-$

$+$

V_o

$-$

CHOPPER
(Variable DC output)

INVERTER
(Switch are turned ON/OFF
with square-wave patterns)

- DC link voltage is varied by a DC-to DC converter or controlled rectifier.

- Generate "square wave" output voltage.

- Output voltage amplitude is varied as DC link is varied.

- Frequency of output voltage is varied by changing the frequency of the square wave pulses.

Variable DC link inverter (2)

- Advantages:
  - simple waveform generation
  - Reliable

- Disadvantages:
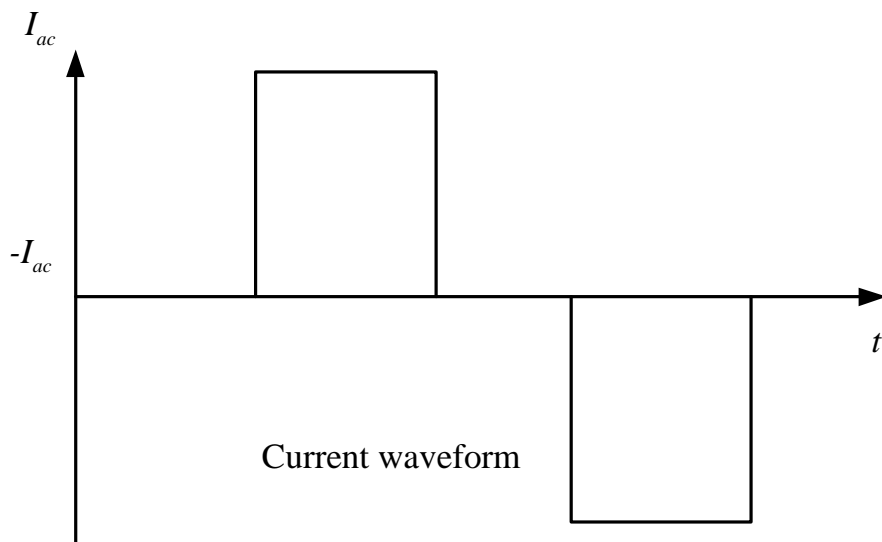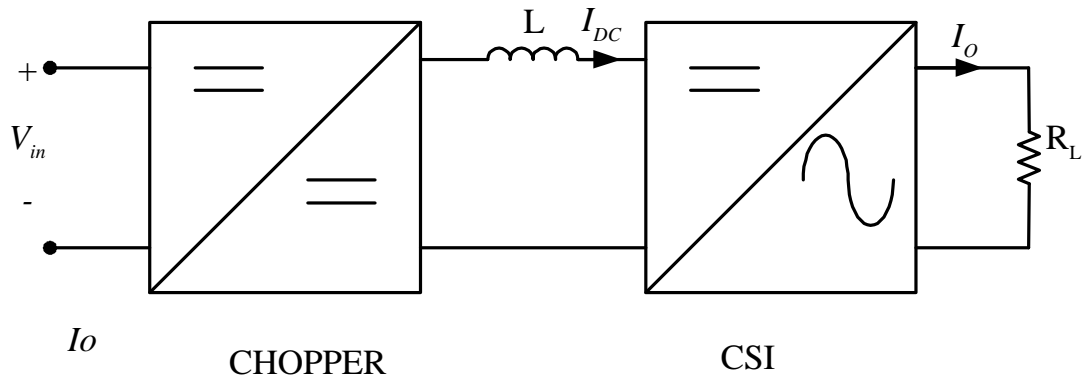 - Extra conversion stage
  - Poor harmonics

VSI with fixed DC link

INVERTER

Switch turned ON and OFF
with PWM pattern
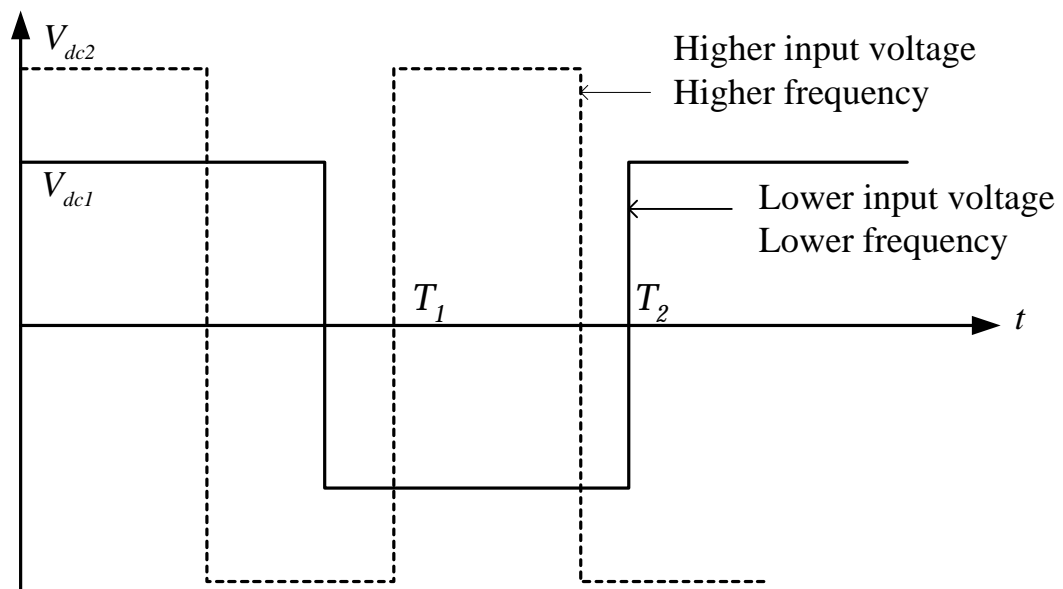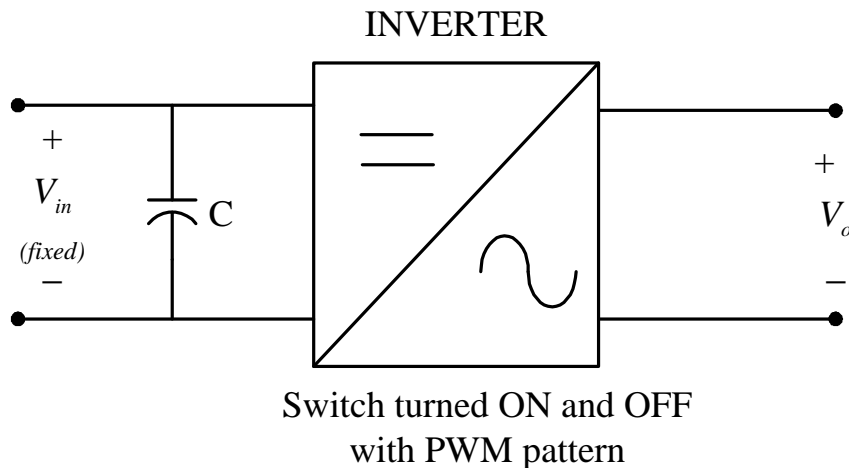
- DC voltage is held constant.

- Output voltage amplitude and frequency are varied simultaneously using PWM technique.

- Good harmonic control, but at the expense of complex waveform generation

Current Source Inverter (CSI)

CHOPPER

CSI

Current waveform

- Input (DC) current is "chopped" to obtain AC output current.
- Need large L.
- Less popular compared to VSI
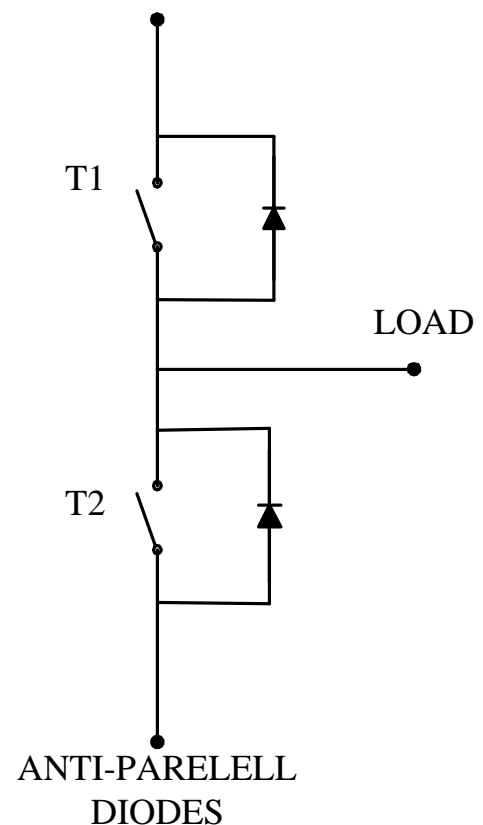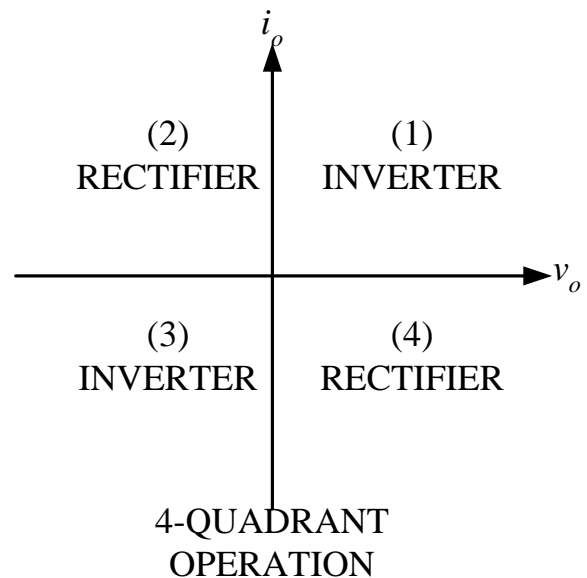
Power flow consideration
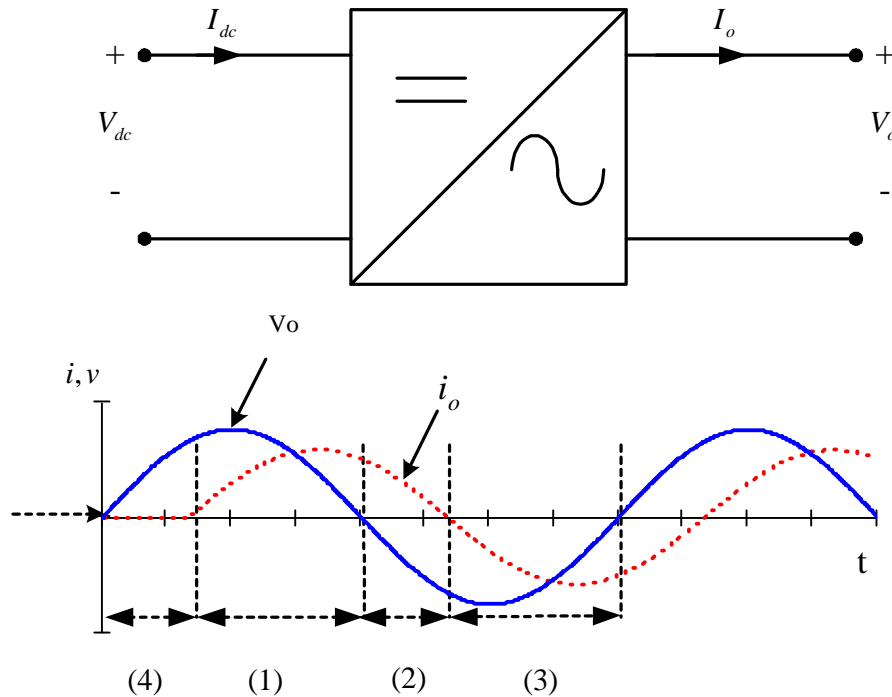
- Assume load is drawing lagging Power Factor.:
- (+) i_o and (+) v_o: (+) power flow   (1)
- (-) i_o and (-) v_o: (+) power flow   (3)
- (+) i_o and (-) v_o: (-) power flow   (2)
- (-) i_o and (+) v_o: (-) power flow   (4)
- Positive power flow indicates power transfer from input ($V_{dc}.I_{dc}$) to load.
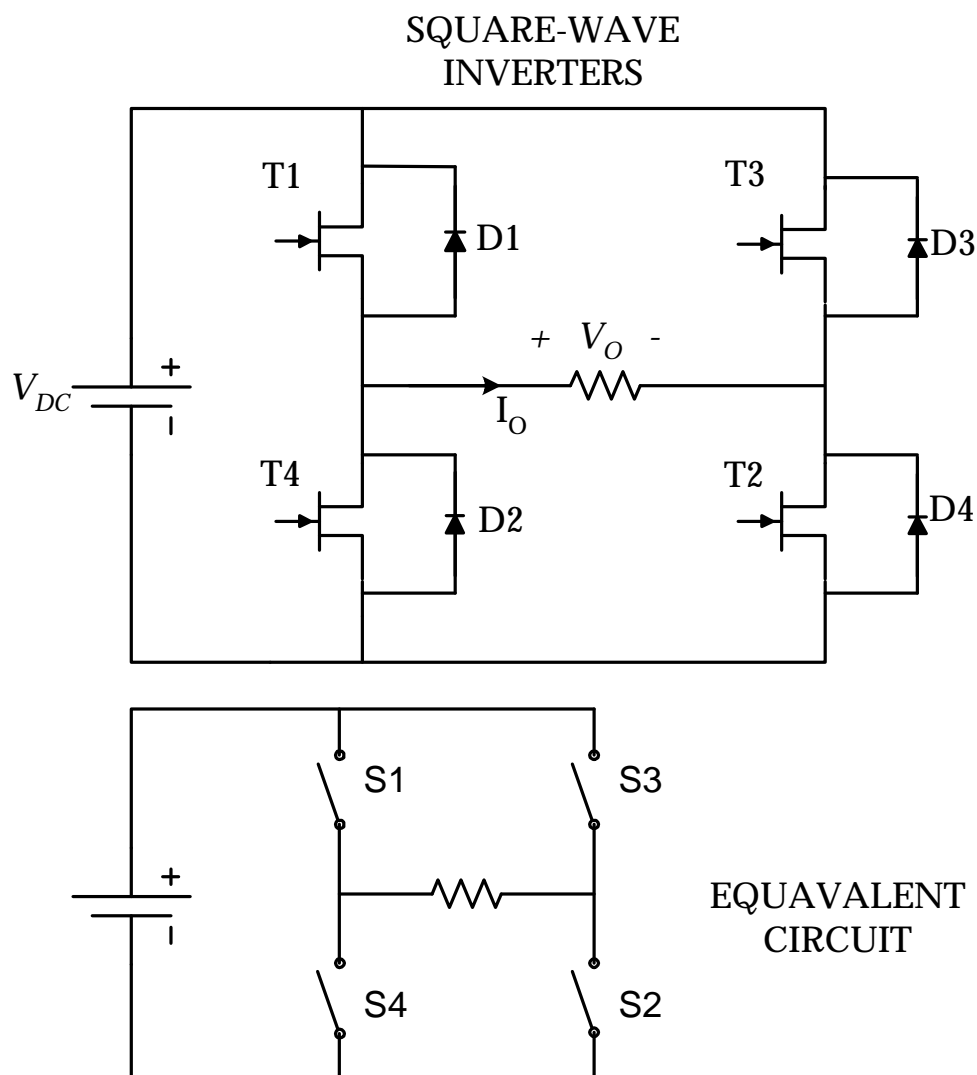
4-quadrant operation

- Negative power flow indicates that the power is fed back from load to source.

- Hence, inverter must have "4 quadrant" capability to cater for all possible load types.

- Practically, this can be achieved by placing an anti-parallel diode across each switching device.

i_o

(2) RECTIFIER   (1) INVERTER

v_o

(3) INVERTER   (4) RECTIFIER

4-QUADRANT OPERATION

T1

T2

LOAD

ANTI-PARELELL DIODES

Operation of simple square-wave inverter (1)

- To illustrate the concept of AC waveform generation

SQUARE-WAVE
INVERTERS

EQUAVALENT
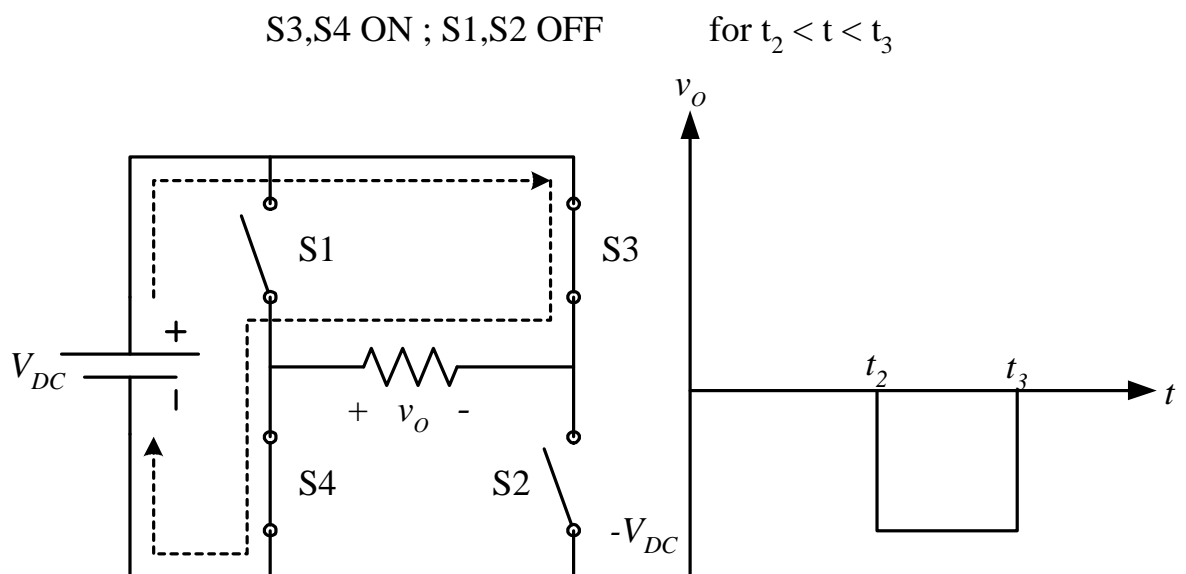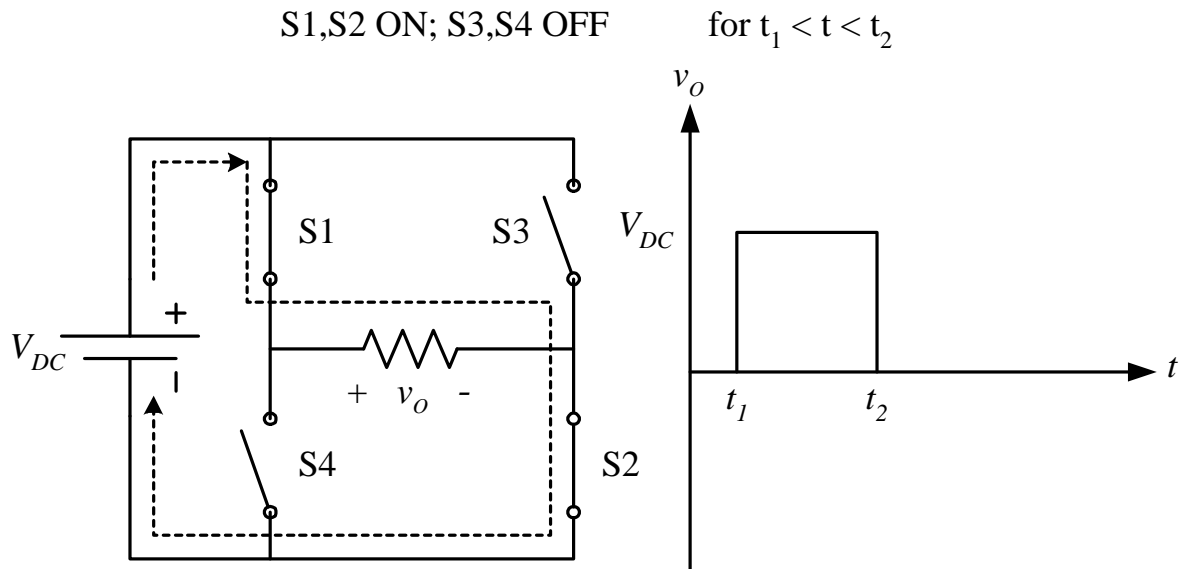CIRCUIT

Operation of simple square-wave inverter (2)

S1,S2 ON; S3,S4 OFF   for $t_1 < t < t_2$

S3,S4 ON ; S1,S2 OFF   for $t_2 < t < t_3$

Waveforms and harmonics of square-wave inverter

INVERTER
OUTPUT

V_{dc}

$-V_{dc}$

FUNDAMENTAL

V_1

$$\frac{4V_{DC}}{p}$$

3RD HARMONIC

$\dfrac{V_1}{3}$

5RD HARMONIC

$\dfrac{V_1}{5}$
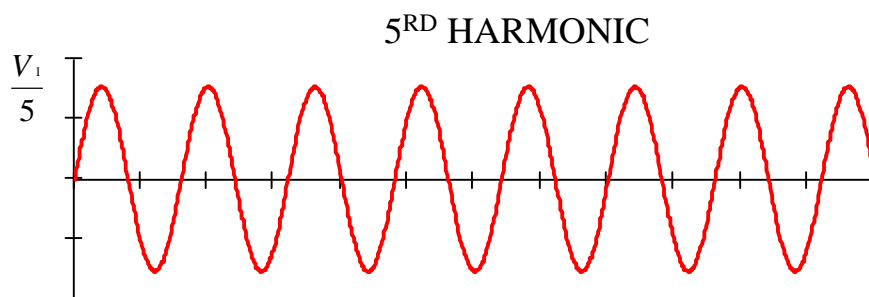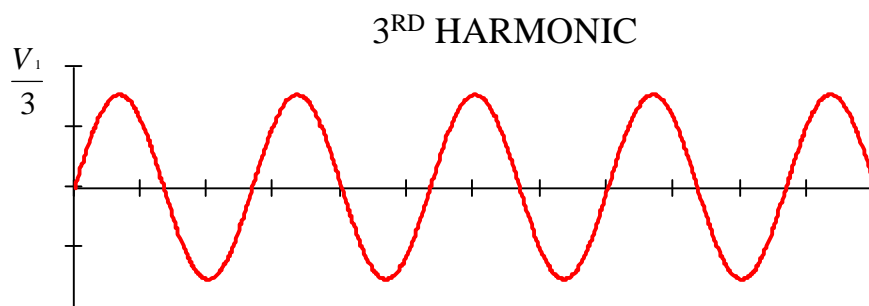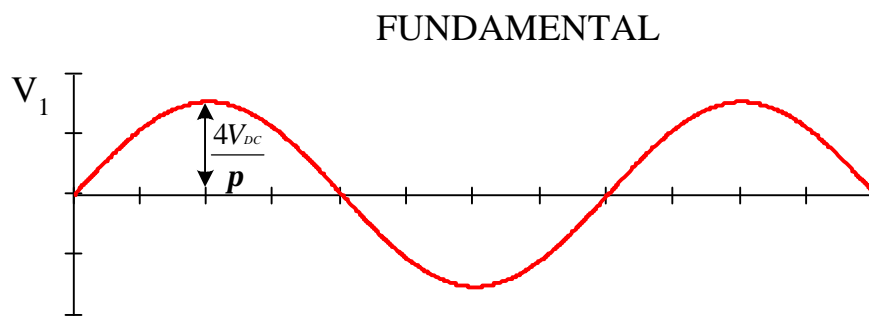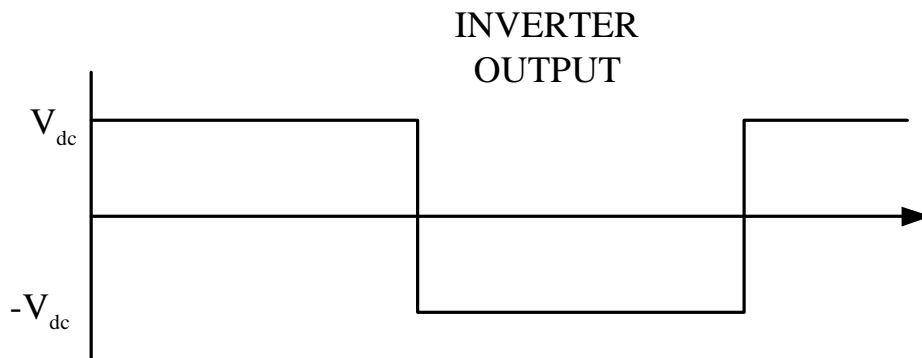
Filtering (1)

- Output of the inverter is "chopped AC voltage with zero DC component".In some applications such as UPS, *"high purity"* sine wave output is required.

- An LC section low-pass filter is normally fitted at the inverter output to reduce the high frequency harmonics.

- In some applications such as AC motor drive, filtering is not required.

(LOW PASS) FILTER

L

+

v_{o1}

C

+

v_{o2}

LOAD

−

−

v_{o1}

v_{o2}
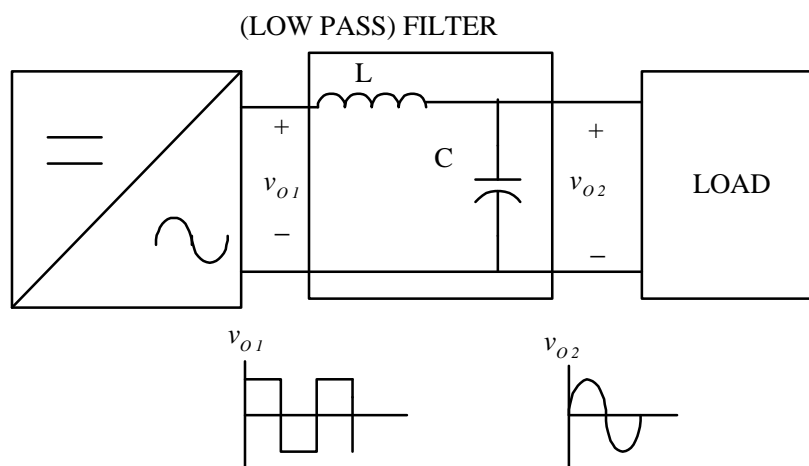
Notes on low-pass filters

- In square wave inverters, maximum output voltage is achievable. However there in NO control in harmonics and output voltage magnitude.

- The harmonics are always at three, five, seven etc times the fundamental frequency.

- Hence the cut-off frequency of the low pass filter is somewhat fixed. The filter size is dictated by the VA ratings of the inverter.

- To reduce filter size, the *PWM switching scheme* can be utilised.

- In this technique, the harmonics are "pushed" to higher frequencies. Thus the cut-off frequency of the filter is increased. Hence the filter components (I.e. L and C) sizes are reduced.

- The trade off for this flexibility is complexity in the switching waveforms.

"Notching" of square wave

Notched Square Wave

Vdc

−Vdc

Fundamental Component

Vdc

−Vdc
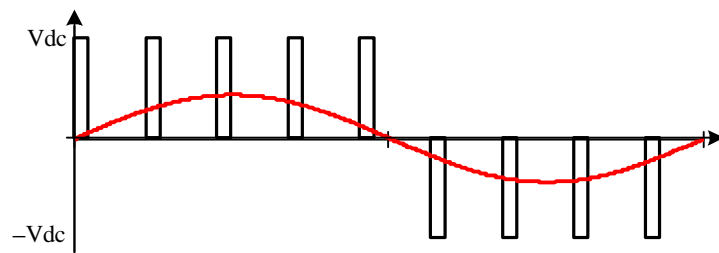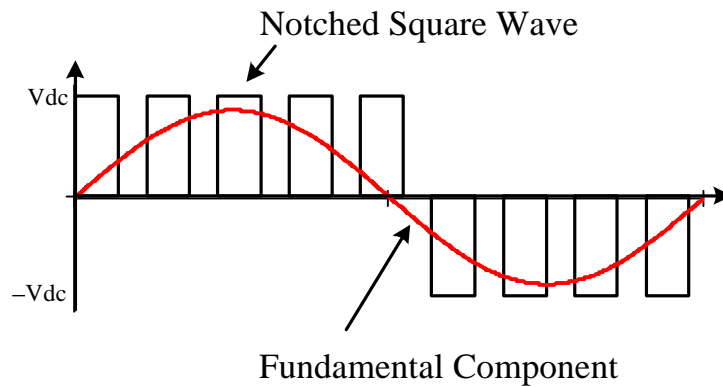
- Notching results in controllable output voltage magnitude (compare Figures above).

- Limited degree of harmonics control is possible

Pulse-width modulation (PWM)

- A better square wave notching is shown below - this is known as PWM technique.

- Both amplitude and frequency can be controlled independently. Very flexible.

pwm waveform

desired sinusoid
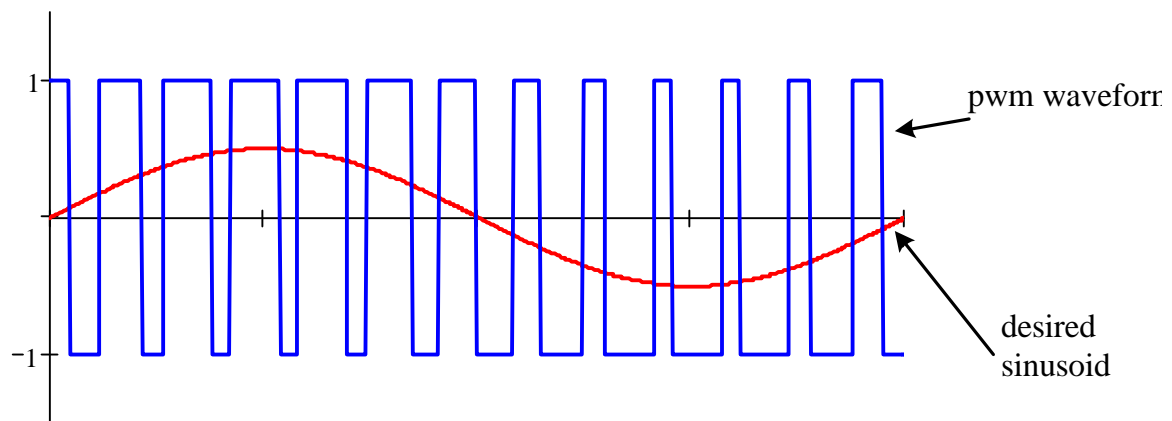
SINUSOIDAL PULSE-WITDH MODULATED
APPROXIMATION TO SINE WAVE

PWM- output voltage and frequency control

Output voltage harmonics

- Why need to consider harmonics?
  - Waveform quality must match TNB supply. "Power Quality" issue.
  - In some applications, harmonics cause degradation of equipment. Equipment need to be "de-rated".

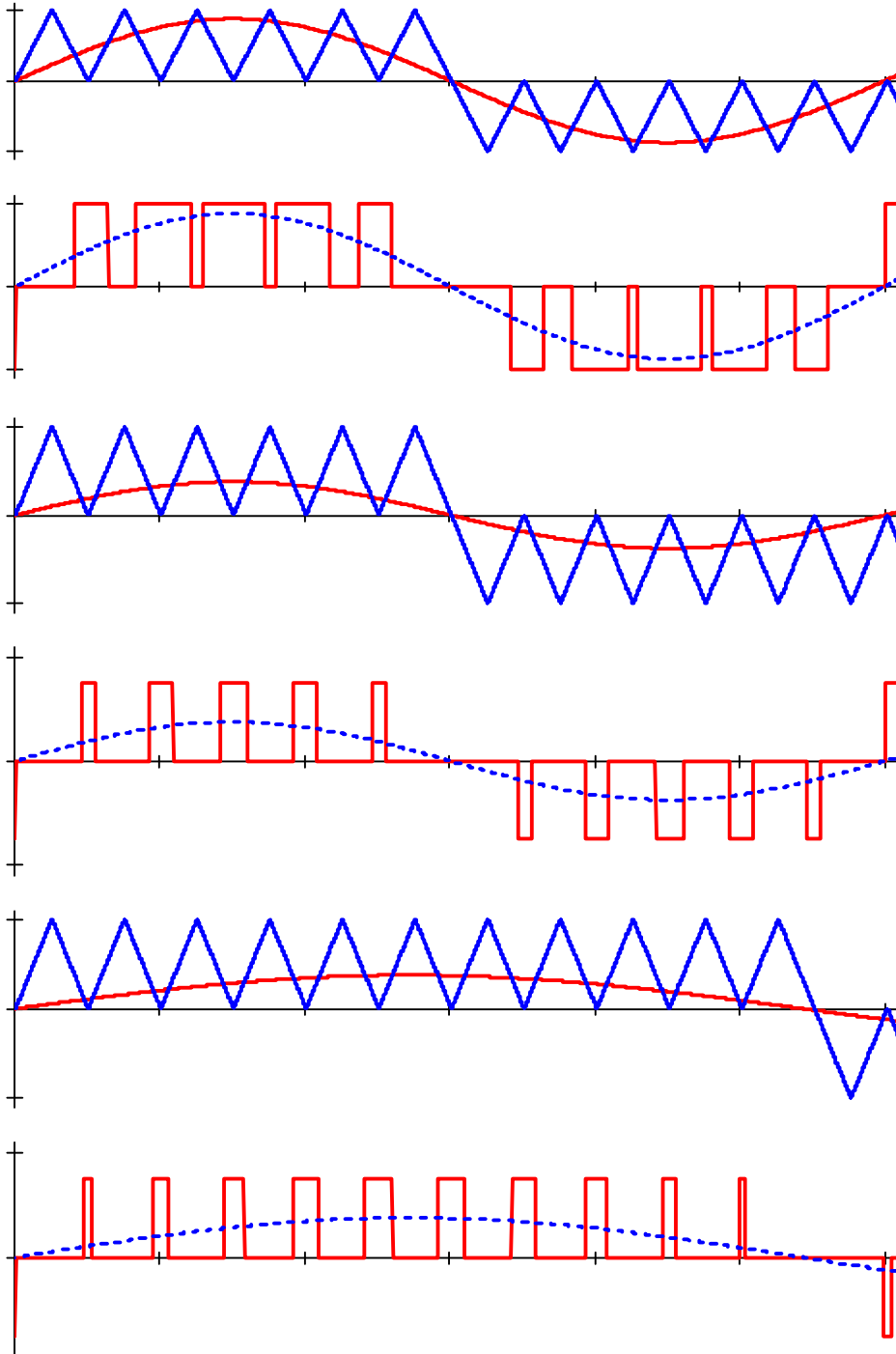- Total Harmonic Distortion (THD):

$$THD = \frac{\sqrt{\sum_{n=2}^{\infty}\left(V_{n,RMS}\right)^2}}{V_{1,RMS}} = \frac{\sqrt{\sum_{n=2}^{\infty}\left(V_{RMS}\right)^2 - \left(V_{1,RMS}\right)^2}}{V_{1,RMS}}$$

where n is the harmonics number.

Current THD can be obtained by replacing

the harmonic voltage with harmonic current :

$$I_n = \frac{V_n}{Z_n}$$

Z_n is the impedance at harmonic frequency.

Fourier Series

- Study of harmonics requires understanding of wave shapes. Fourier Series is a tool to analyse wave shapes.

Fourier Series

$$a_o = \frac{1}{p} \int_0^{2p} f(v) dq$$

$$a_n = \frac{1}{p} \int_0^{2p} f(v) \cos(nq) dq$$

$$b_n = \frac{1}{p} \int_0^{2p} f(v) \sin(nq) dq$$

Inverse Fourier

$$f(v) = \frac{1}{2} a_o + \sum_{n=1}^{\infty} (a_n \cos nq + b_n \sin nq)$$

where $\quad q = wt$

Harmonics of square-wave (1)
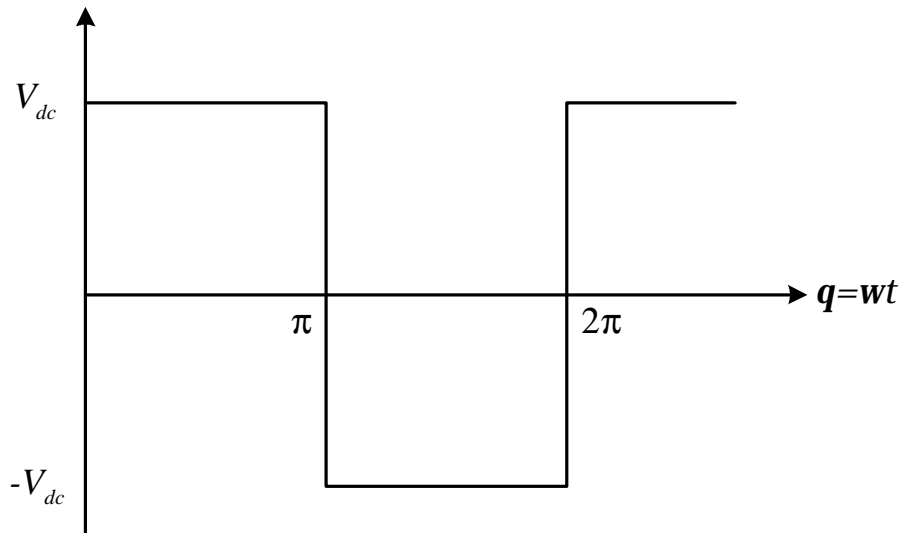
$$a_o = \frac{1}{p}\left[\int_0^p V_{dc}\,dq + \int_p^{2p} -V_{dc}\,dq\right] = 0$$

$$a_n = \frac{V_{dc}}{p}\left[\int_0^p \cos(nq)\,dq - \int_p^{2p} \cos(nq)\,dq\right] = 0$$

$$b_n = \frac{V_{dc}}{p}\left[\int_0^p \sin(nq)\,dq - \int_p^{2p} \sin(nq)\,dq\right]$$

Harmonics of square wave (2)

Solving,

$$b_n = \frac{V_{dc}}{n\boldsymbol{p}}\left[-\cos(n\boldsymbol{q})\Big|_0^{\boldsymbol{p}} + \cos(n\boldsymbol{q})\Big|_{\boldsymbol{p}}^{2\boldsymbol{p}}\right]$$

$$= \frac{V_{dc}}{n\boldsymbol{p}}\left[(\cos 0 - \cos n\boldsymbol{p}) + (\cos 2n\boldsymbol{p} - \cos n\boldsymbol{p})\right]$$

$$= \frac{V_{dc}}{n\boldsymbol{p}}\left[(1 - \cos n\boldsymbol{p}) + (1 - \cos n\boldsymbol{p})\right]$$

$$= \frac{2V_{dc}}{n\boldsymbol{p}}\left[(1 - \cos n\boldsymbol{p})\right]$$

when n is even, $\cos n\boldsymbol{p} = 1$
$b_{n=}0$

when n is odd, $\cos n\boldsymbol{p} = -1$
$b_{n=}\dfrac{4V_{dc}}{n\boldsymbol{p}}$

Spectra of square wave

Normalised
Fundamental
1st

3rd (0.33)

5th (0.2)

7th (0.14)

9th (0.11)

11th (0.09)

1   3   5   7   9   11

n
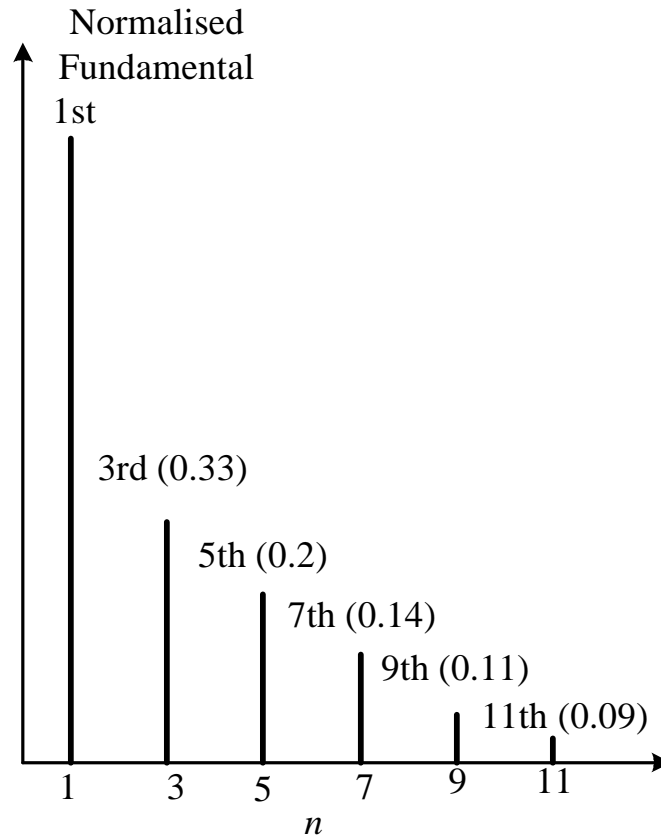
- **Spectra (harmonics) characteristics:**
  - Harmonic decreases as *n* increases. It decreases with a factor of ($1/n$).
  - Even harmonics are absent
  - Nearest harmonics is the 3rd. If fundamental is 50Hz, then nearest harmonic is 150Hz.
  - Due to the small separation between the fundamental an harmonics, output low-pass filter design can be quite difficult.

Quasi-square wave (QSW)
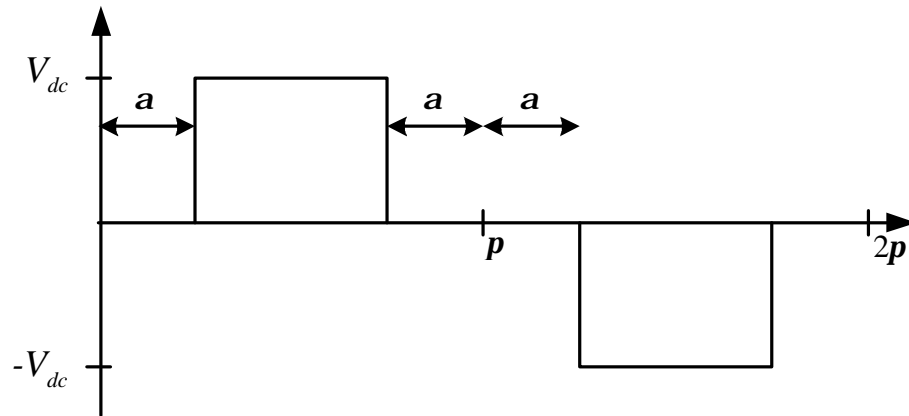
Note that $a_n = 0$.

Due to half - wave symmetry,

$$b_n = 2\left[\frac{1}{p}\int_{a}^{p-a} V_{dc}\sin(nq)dq\right] = \frac{2V_{dc}}{np}\left[-\cos nq\Big|_{a}^{p-a}\right]$$

$$= \frac{2V_{dc}}{np}\left[\cos(na) - \cos n(p-a)\right]$$

Expanding,

$$\cos n(p-a) = \cos(np - na)$$
$$= \cos np \cos na + \sin np \sin na$$
$$= \cos np \cos na$$

Harmonics control

$$\Rightarrow b_n = \frac{2V_{dc}}{np}[\cos(na) - \cos np \cos na]$$

$$= \frac{2V_{dc}}{np}\cos(na)[1 - \cos np]$$

If n is even, $\Rightarrow b_n = 0,$

If n is odd, $\Rightarrow b_n = \frac{4V_{dc}}{np}\cos(na)$

In particular, amplitude of the fundamental is :
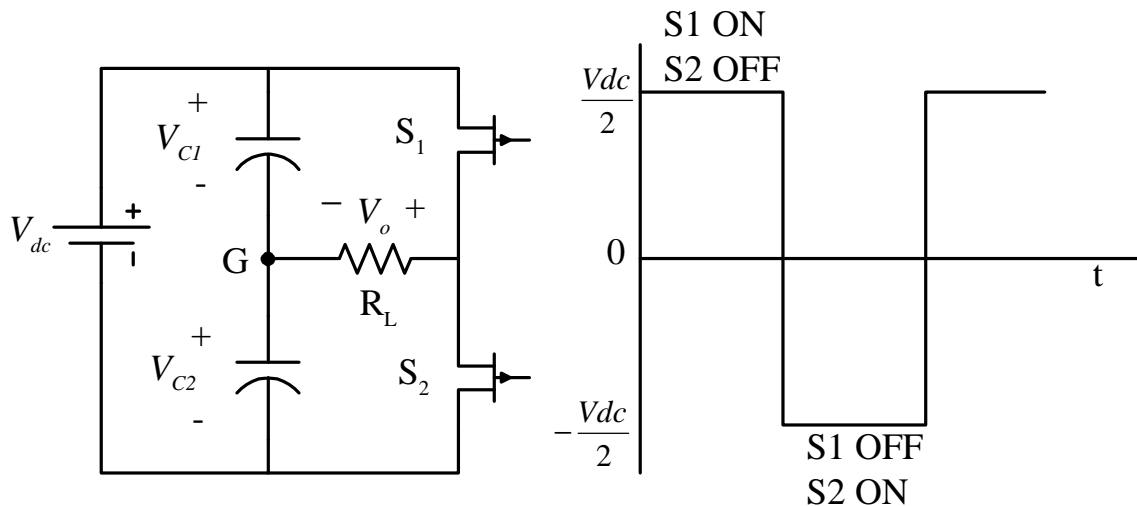
$$b_1 = \frac{4V_{dc}}{p}\cos(a)$$

The fundamental , b_1, is controlled by varying á

Harmonics can also be controlled by adjusting a,
For example if $a = 30^o$, then $b_3 = 0,$ or the third
harmonic is eliminated from the waveform. In
general, harmonic n will be eliminated if :

$$a = \frac{90^o}{n}$$

Half-bridge inverter (1)

- Also known as the "inverter leg".
- Basic building block for full bridge, three phase and higher order inverters.
- G is the "centre point".
- Both capacitors have the same value. Thus the DC link is equally "spilt" into two.

Half-bridge inverter (2)

- The top and bottom switch has to be "complementary", i.e. If the top switch is closed (on), the bottom must be off, and vice-versa.

- In practical, a dead time as shown below is required to avoid "shoot-through" faults.

"Shoot through fault" .
I_{short} is very large

"Dead time' $= t_d$
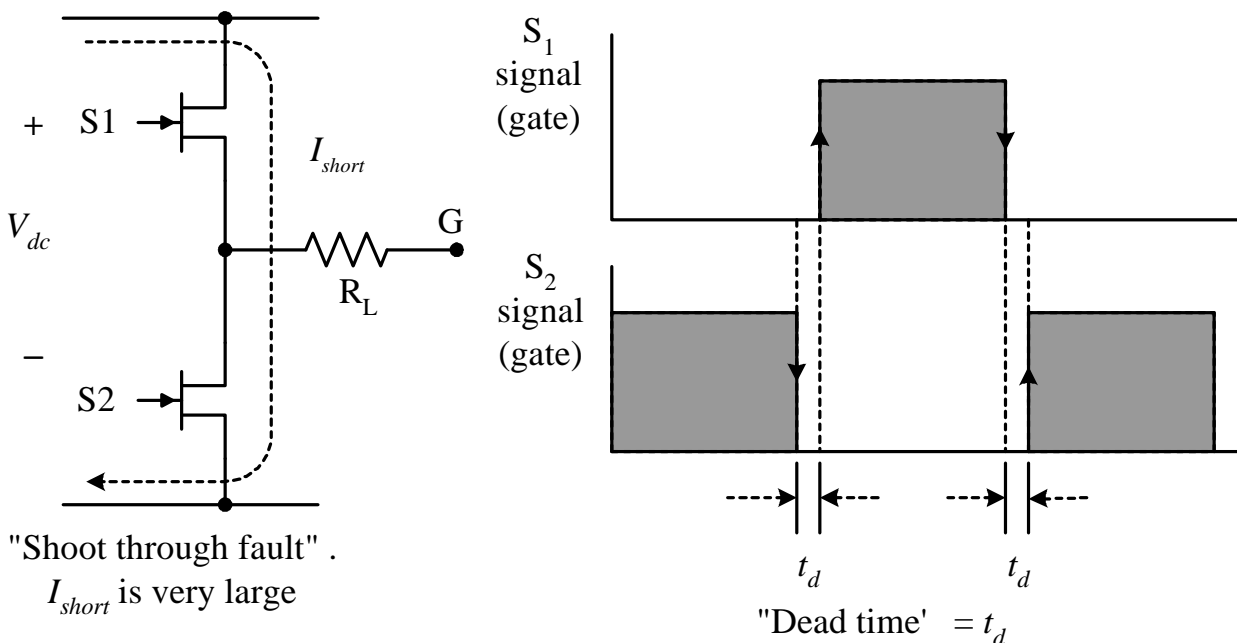
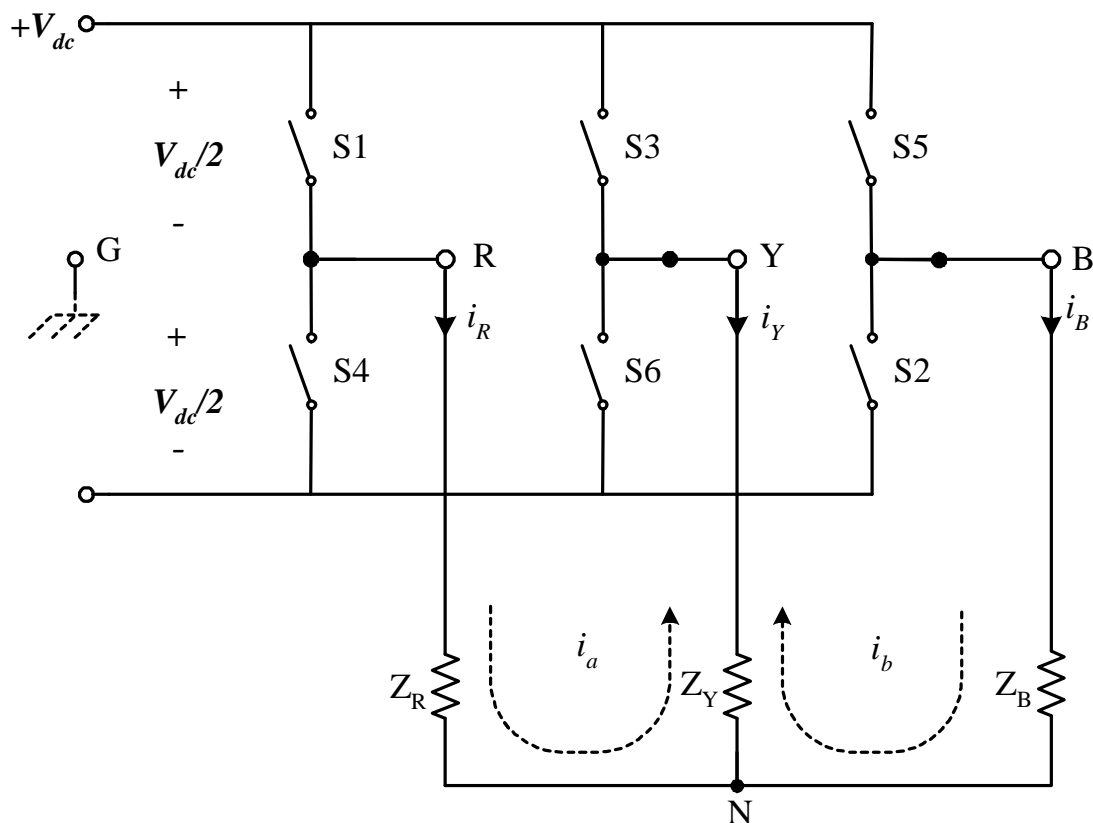# Single-phase, full-bridge (1)

- Full bridge (single phase) is built from two half-bridge leg.
- The switching in the second leg is "delayed by 180 degrees" from the first leg.

LEG R   LEG R'

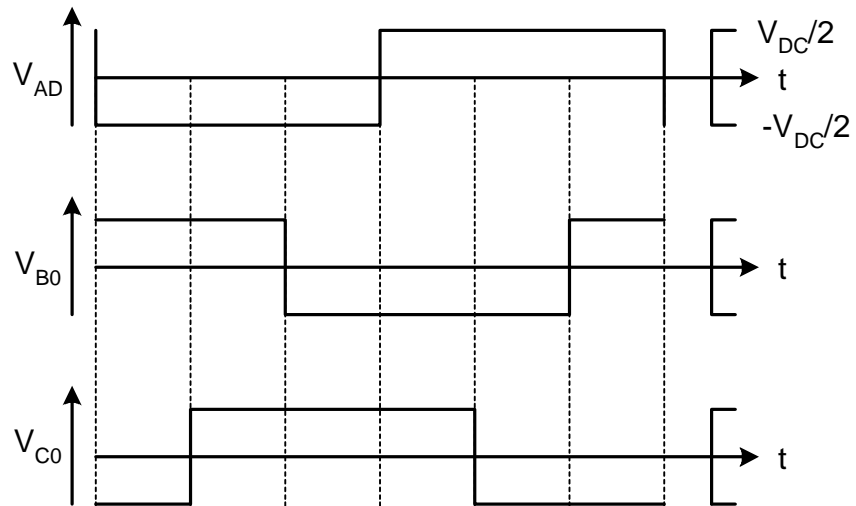$$V_o = V_{RG} - V_{R'G}$$

G is "virtual groumd"
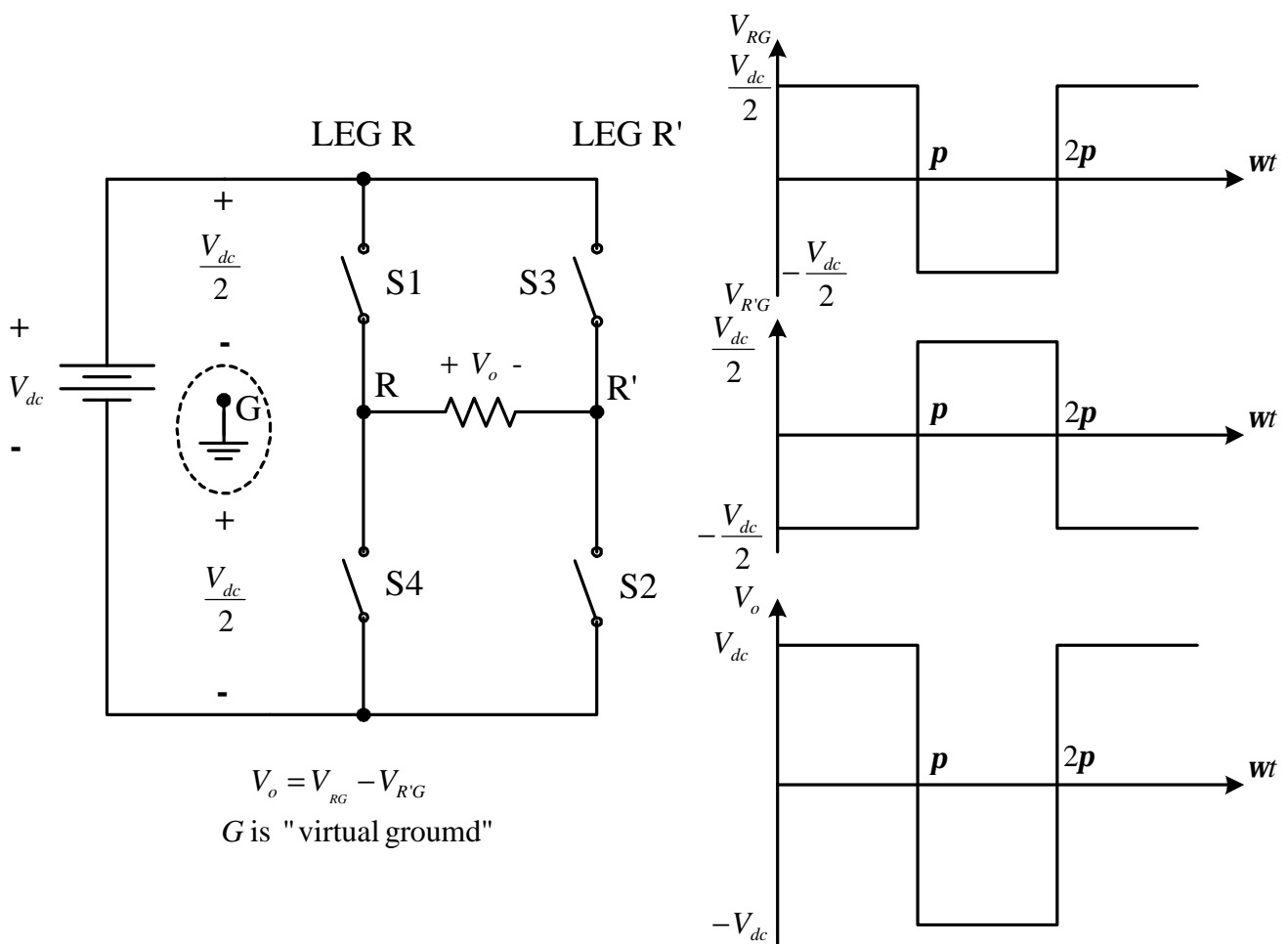
Three-phase inverter

- Each leg (Red, Yellow, Blue) is delayed by 120 degrees.

- A three-phase inverter with star connected load is shown below

Square-wave inverter waveforms

(a) Three phase pole switching waveforms
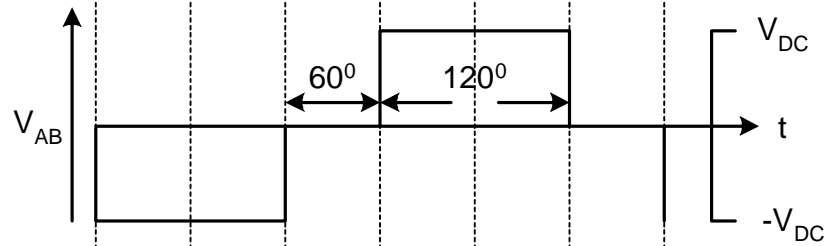
(b) Line voltage waveform

(c) Phase voltage waveform (six-step)

Interval	1	2	3	4	5	6
Positive device(s) on	3	3,5	5	1,5	1	1,3
Negative devise(s) on	2,4	4	4,6	6	2,6	2

Quasi-square wave operation voltage waveforms

Three-phase inverter waveform relationship

- V_{RG}, V_{YG}, V_{BG} are known as "pole switching waveform" or "inverter phase voltage".
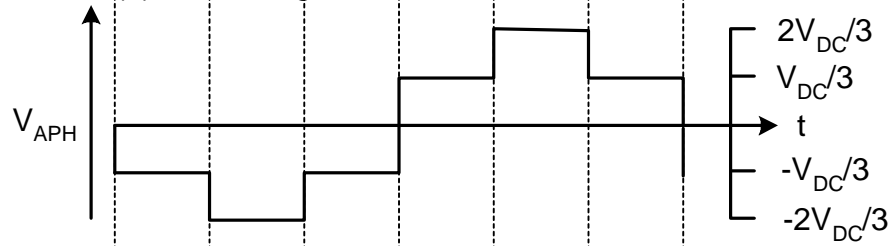
- V_{RY}, V_{RB}, V_{YB} are known as "line to line voltage" or simply "line voltage".

- For a three-phase star-connected load, the load phase voltage with respect to the "N" (star-point) potential is known as V_{RN}, V_{YN}, V_{BN}. It is also popularly termed as "six-step" waveform

MODULATION: Pulse Width Modulation (PWM)
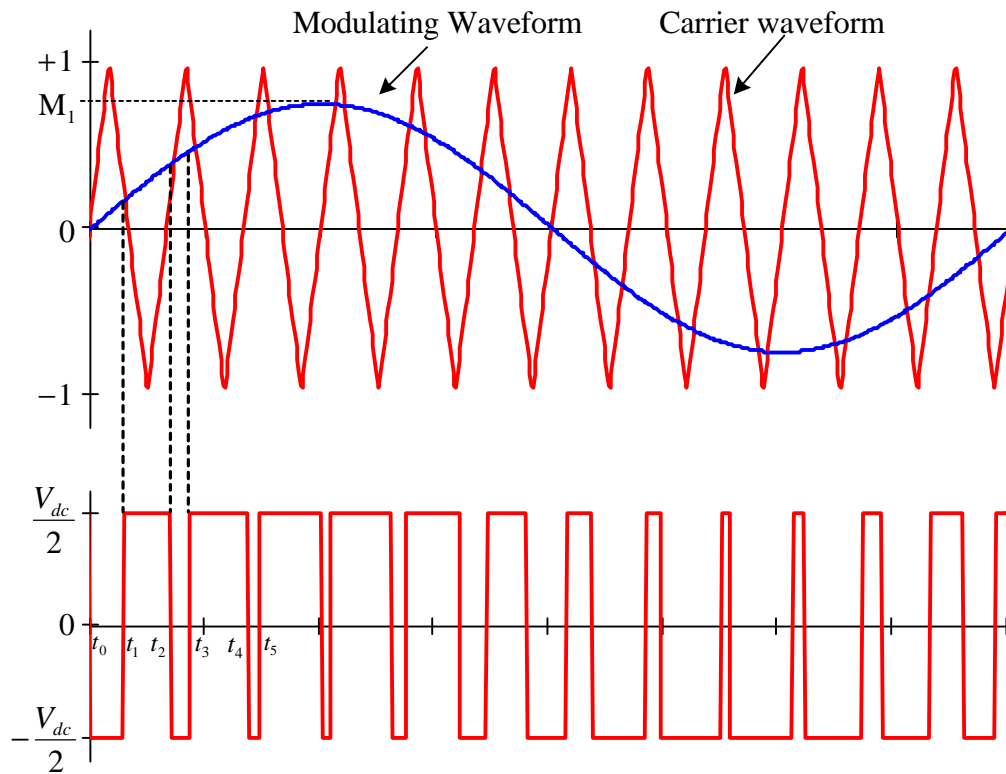
- **Triangulation method (Natural sampling)**
  - Amplitudes of the triangular wave (carrier) and sine wave (modulating) are compared to obtain PWM waveform. Simple analogue comparator can be used.
  - Basically an analogue method. Its digital version, known as REGULAR sampling is widely used in industry.

PWM types

- Natural (sinusoidal) sampling (as shown on previous slide)
  - Problems with analogue circuitry, e.g. Drift, sensitivity etc.

- Regular sampling
  - simplified version of natural sampling that results in simple digital implementation

- Optimised PWM
  - PWM waveform are constructed based on certain performance criteria, e.g. THD.

- Harmonic elimination/minimisation PWM
  - PWM waveforms are constructed to eliminate some undesirable harmonics from the output waveform spectra.
  - Highly mathematical in nature

- Space-vector modulation (SVM)
  - A simple technique based on volt-second that is normally used with three-phase inverter motor-drive

Natural/Regular sampling

MODULATION INDEX $= M_I$:

$$M_I = \frac{\text{Amplitude of the modulating waveform}}{\text{Amplitude of the carrier waveform}}$$

M_I is related to the fundamental (sine wave) output voltage magnitude. If M_I is high, then the sine wave output is high and vice versa.

If $0 < M_I < 1$, the linear relationship holds :

$$V_1 = M_I V_{in}$$

where V_1, V_{in} are fundamental of the output voltage and input (DC) voltage, respectively.
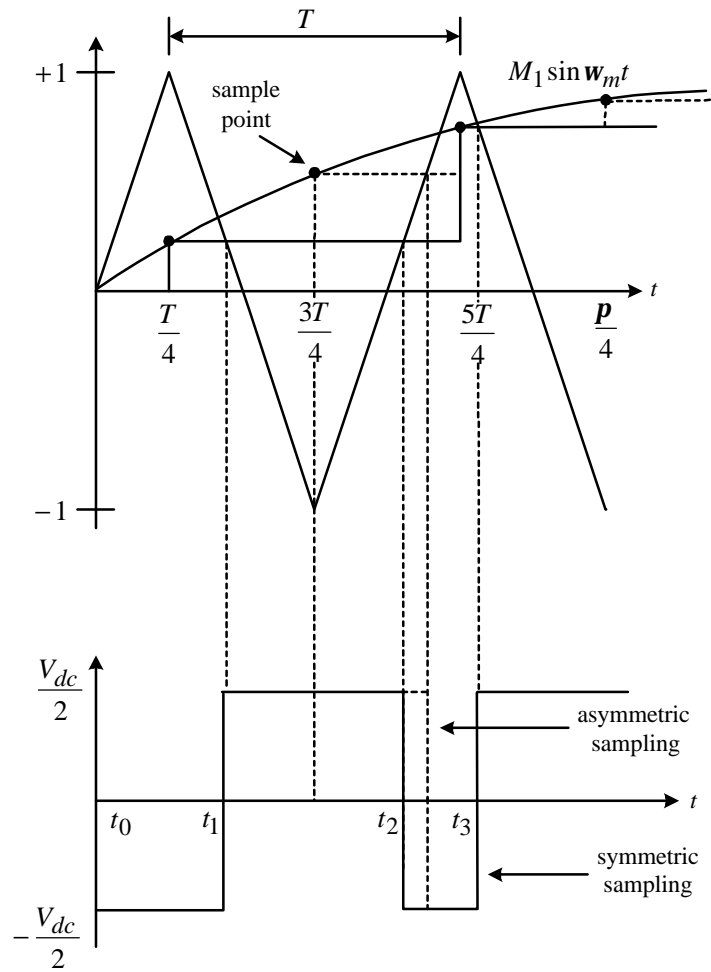
MODULATION RATIO $= M_R (= p)$

$$M_R = \frac{\text{Frequency of the modulating waveform}}{\text{Amplitude of the carrier waveform}}$$

M_R is related to the "harmonic frequency". The harmonics are normally located at :

$$f = k M_R (f_m)$$

where f_m is the frequency of the modulating signal and k is an integer (1,2,3...)

Asymmetric and symmetric regular sampling

Generating of PWM waveform regular sampling

Bipolar and unipolar PWM switching scheme

- In many books, the term "bipolar" and "unipolar" PWM switching are often mentioned.

- The difference is in the way the sinusoidal (modulating) waveform is compared with the triangular.

- In general, unipolar switching scheme produces better harmonics. But it is more difficult to implement.

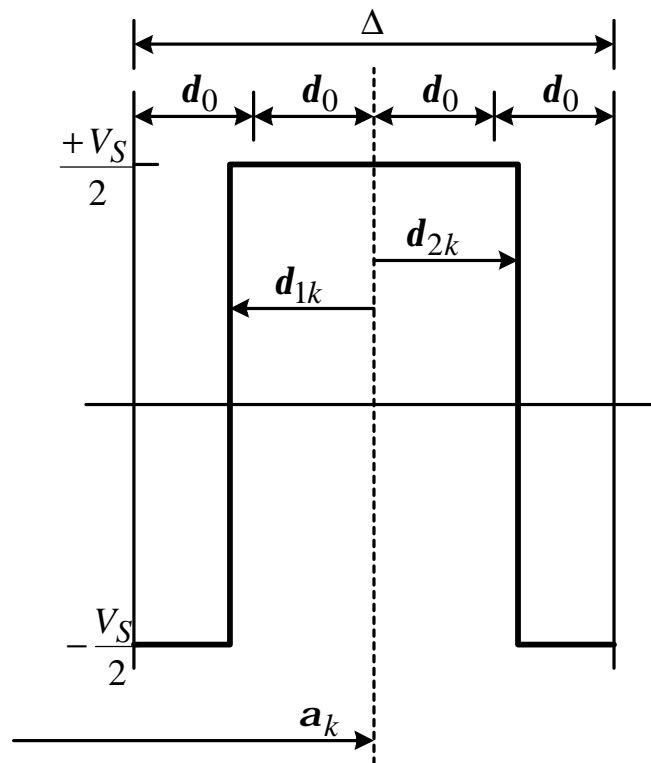- In this class only bipolar PWM is considered.

Bipolar PWM switching
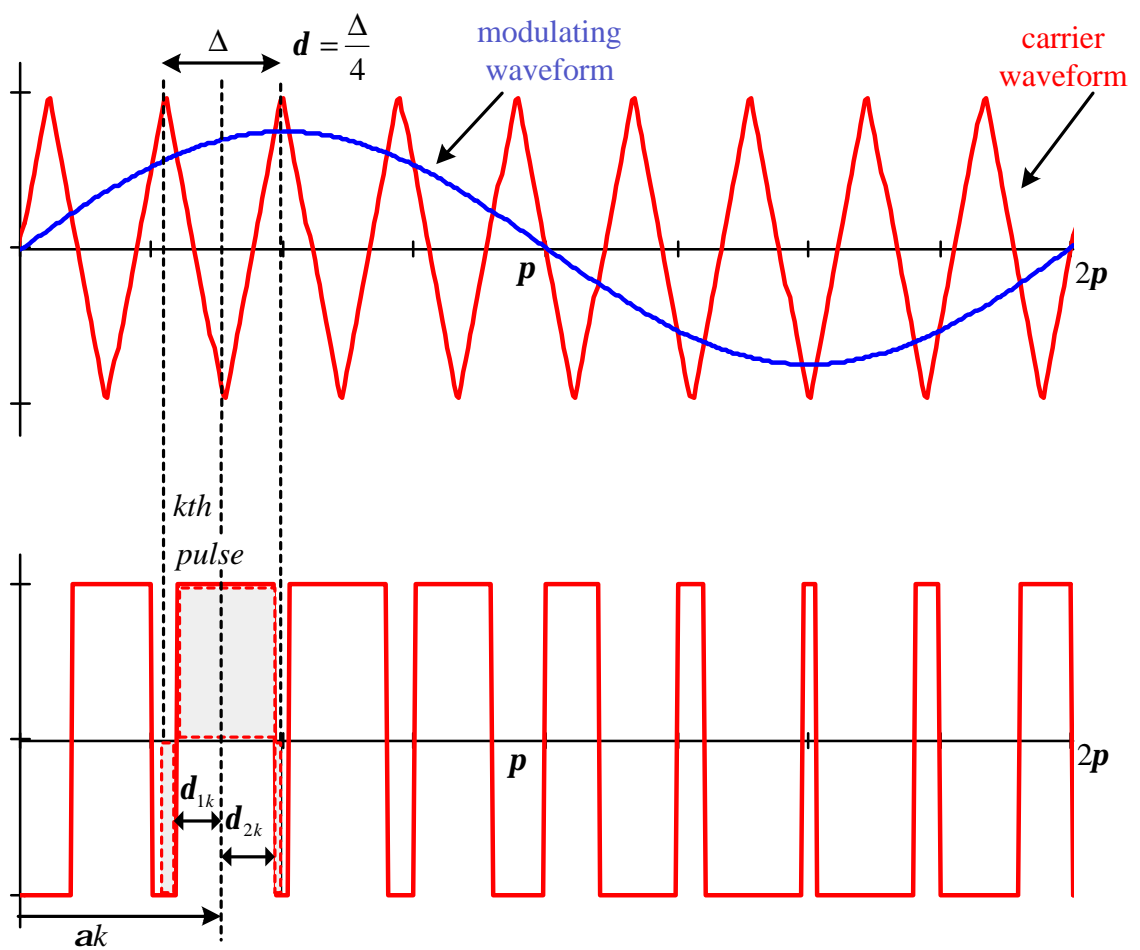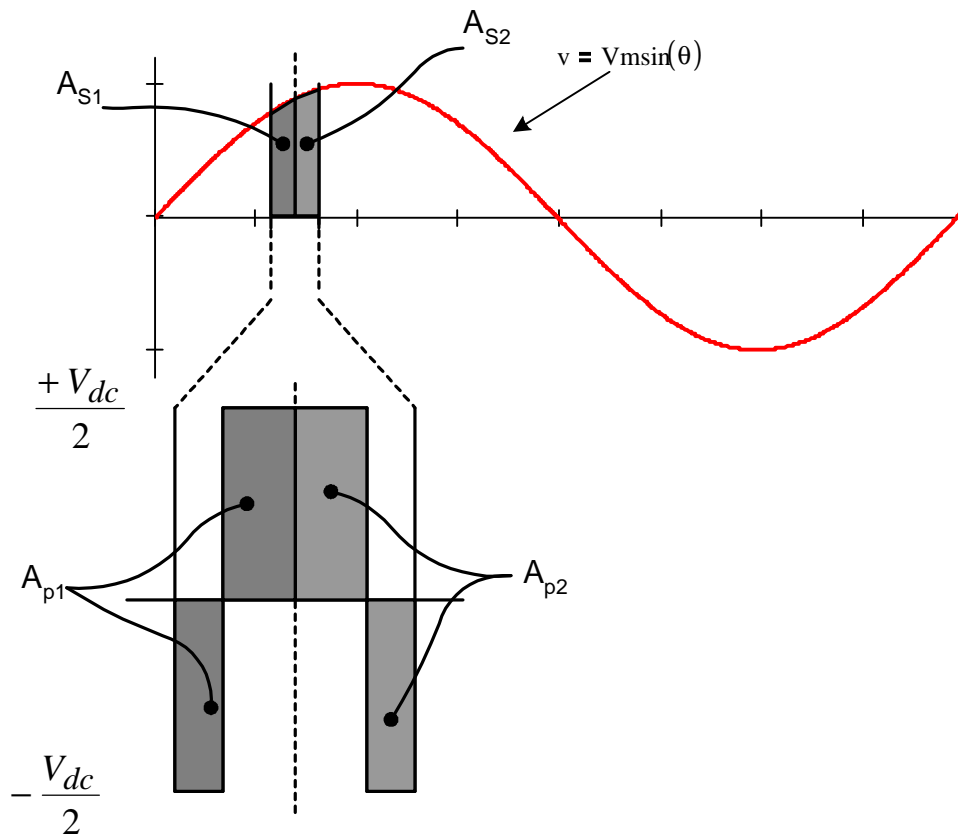
Pulse width relationships
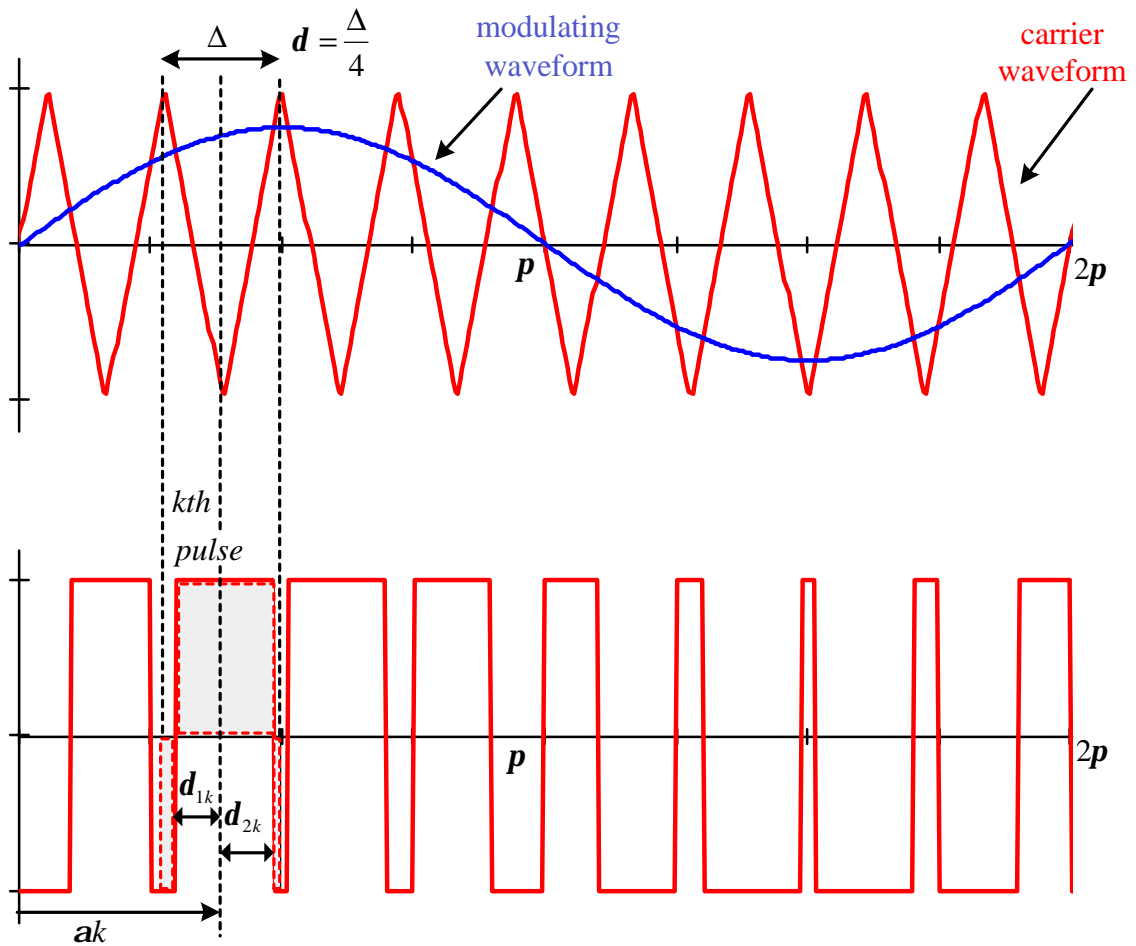
Characterisation of PWM pulses for bipolar switching

The kth PWM pulse

Determination of switching angles for kth PWM pulse (1)

Equating the volt - second,

$$A_{s1} = A_{p1}$$

$$A_{s2} = A_{p2}$$

PWM Switching angles (2)

The average voltage during each half cycle of the PWM pulse is given as :

$$\overline{V}_{1k} = \left(\frac{V_{dc}}{2}\right)\left(\frac{d_{1k}-(2d_o-d_{1k})}{2d_o}\right)$$

$$= \left(\frac{V_{dc}}{2}\right)\left(\frac{d_{1k}-d_o}{d_o}\right) = b_{1k}\left(\frac{V_s}{2}\right)$$

where $b_{1k} = \left(\dfrac{d_{1k}-d_o}{d_o}\right)$

Similarly,

$$\overline{V}_{2k} = b_{2k}\left(\frac{V_{dc}}{2}\right); \text{ where } b_{2k} = \left(\frac{d_{2k}-d_o}{d_o}\right)$$

The volt - second supplied by the sinusoid,

$$A_{s1} = \int_{a_k-2d_o}^{a_k} V_m \sin q \, dq = V_m\left[\cos(a_k-2d_o)-\cos a_k\right]$$

$$= 2V_m \sin d_o \sin(a_k-d_o)$$

Switching angles (3)

Since,

$$\sin \boldsymbol{d}_o \rightarrow \boldsymbol{d}_o \quad \text{for small} \, \boldsymbol{d}_o \,,$$

$$A_{s1} = 2\boldsymbol{d}_o V_m \sin(\boldsymbol{a}_k - \boldsymbol{d}_o)$$

Similarly,

$$A_{s2} = 2\boldsymbol{d}_o V_m \sin(\boldsymbol{a}_k + \boldsymbol{d}_o)$$

The volt - seconds of the PWM waveforms,

$$A_{p1} = \boldsymbol{b}_{1k}\left(\frac{V_{dc}}{2}\right)2\boldsymbol{d}_o; \qquad A_{p2} = \boldsymbol{b}_{21k}\left(\frac{V_{dc}}{2}\right)2\boldsymbol{d}_o$$

To derive the modulation strategy,

$$A_{p1} = A_{s1}; \qquad A_{p2} = A_{s2}$$

Hence, for the leading edge

$$\boldsymbol{b}_{1k}\left(\frac{V_{dc}}{2}\right)2\boldsymbol{d}_o = 2\boldsymbol{d}_o V_m \sin(\boldsymbol{a}_k - \boldsymbol{d}_o)$$

$$\Rightarrow \boldsymbol{b}_{1k} = \frac{V_m}{(V_{dc}/2)}\sin(\boldsymbol{a}_k - \boldsymbol{d}_o)$$

PWM switching angles (4)

The voltage ratio,

$$M_I = \frac{V_m}{(V_{dc}/2)}$$ is known as modulation

index or depth. It varies from 0 to 1.

Thus,

$$\boldsymbol{b}_{1k} = M_I \sin(\boldsymbol{a}_k - \boldsymbol{d}_o)$$

Using similar method, the trailing edge can be derived :

$$\boldsymbol{b}_{2k} = M_I \sin(\boldsymbol{a}_k - \boldsymbol{d}_o)$$

Substituting to solve for the pulse - width,

$$\boldsymbol{b}_{1k} = \frac{\boldsymbol{d}_{1k} - \boldsymbol{d}_o}{\boldsymbol{d}_o}$$

$$\Rightarrow \boldsymbol{d}_{1k} = \boldsymbol{d}_o \left[1 + M_I \sin(\boldsymbol{a}_k - \boldsymbol{d}_o)\right]$$

and

$$\boldsymbol{d}_{2k} = \boldsymbol{d}_o \left[1 + M_I \sin(\boldsymbol{a}_k + \boldsymbol{d}_o)\right]$$

PWM Pulse width

Thus the switching angles of the kth pulse is :

Leading edge : $\boldsymbol{a}_k - \boldsymbol{d}_{1k}$

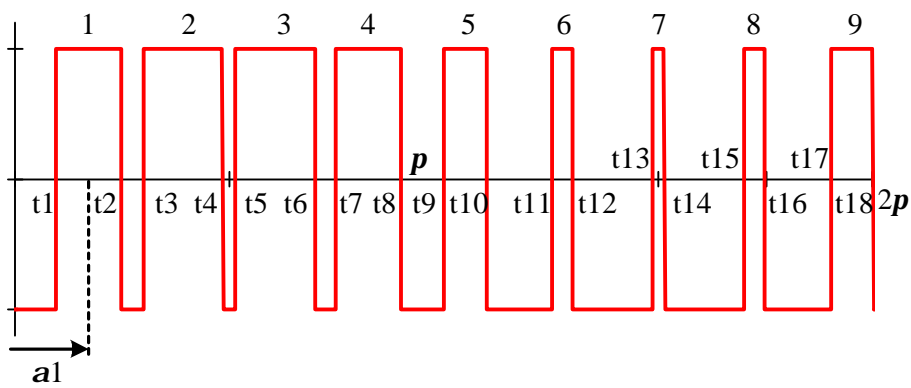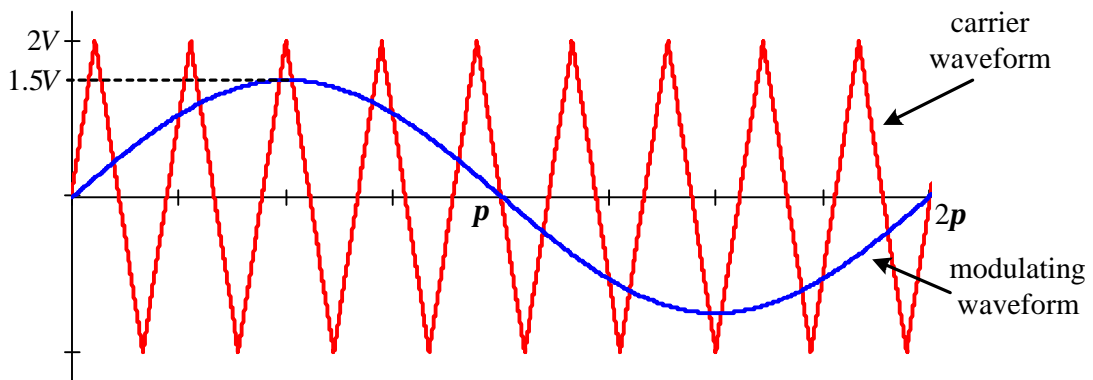Trailing edge : $\boldsymbol{a}_k + \boldsymbol{d}_{1k}$

The above equation is valid for Asymmetric Modulation, i.e $\boldsymbol{d}_{1k}$ and $\boldsymbol{d}_{2k}$ are different.
For Symmetric Modulation, $\boldsymbol{d}_{1k} = \boldsymbol{d}_{2k} = \boldsymbol{d}_k$

$$\Rightarrow \boldsymbol{d}_k = \boldsymbol{d}_o \left[1 + M_I \sin \boldsymbol{a}_k \right]$$

Example

- For the PWM shown below, calculate the switching angles for all the pulses.

Harmonics of bipolar PWM

Assuming the PWM waveform is half - wave symmetry, harmonic content of each (kth) PWM pulse can be computed as :

$$b_{nk} = 2\left(\frac{1}{p}\int_0^T f(v)\sin nq\,dq\right)$$

$$= \frac{2}{p}\left\{\int_{a_k-2d_o}^{a_k-d_{1k}}\left(-\frac{V_{dc}}{2}\right)\sin nq\,dq\right\}$$

$$+ \frac{2}{p}\left\{\int_{a_k-d_{1k}}^{a_k+d_{2k}}\left(\frac{V_{dc}}{2}\right)\sin nq\,dq\right\}$$

$$+ \frac{2}{p}\left\{\int_{a_k+d_{2k}}^{a_k+2d_o}\left(-\frac{V_{dc}}{2}\right)\sin nq\,dq\right\}$$

Which can be reduced to :

$$b_{nk} = -\frac{V_{dc}}{np}\{\cos n(a_k - 2d_o) - \cos n(a_k - d_{1k})$$
$$+ \cos n(a_k + d_{2k}) - \cos n(a_k - d_{1k})$$
$$+ \cos n(a_k + d_{2k}) - \cos n(a_k + 2d_o)\}$$

Harmonics of PWM

Yeilding,

$$b_{nk} = \frac{2V_{dc}}{np} \left[\cos n(\boldsymbol{a}_k - \boldsymbol{d}_{1k}) - \cos n(\boldsymbol{a}_k - 2_{1k}) + 2\cos n\boldsymbol{a}_k \cos n2\boldsymbol{d}_o \right]$$

This equation cannot be simplified productively. The Fourier coefficent for the PWM waveform is the sum of b_{nk} for the p pulses over one period, i.e.:

$$b_n = \sum_{k=1}^{p} b_{nk}$$

The slide on the next page shows the computation of this equation.

PWM Spectra
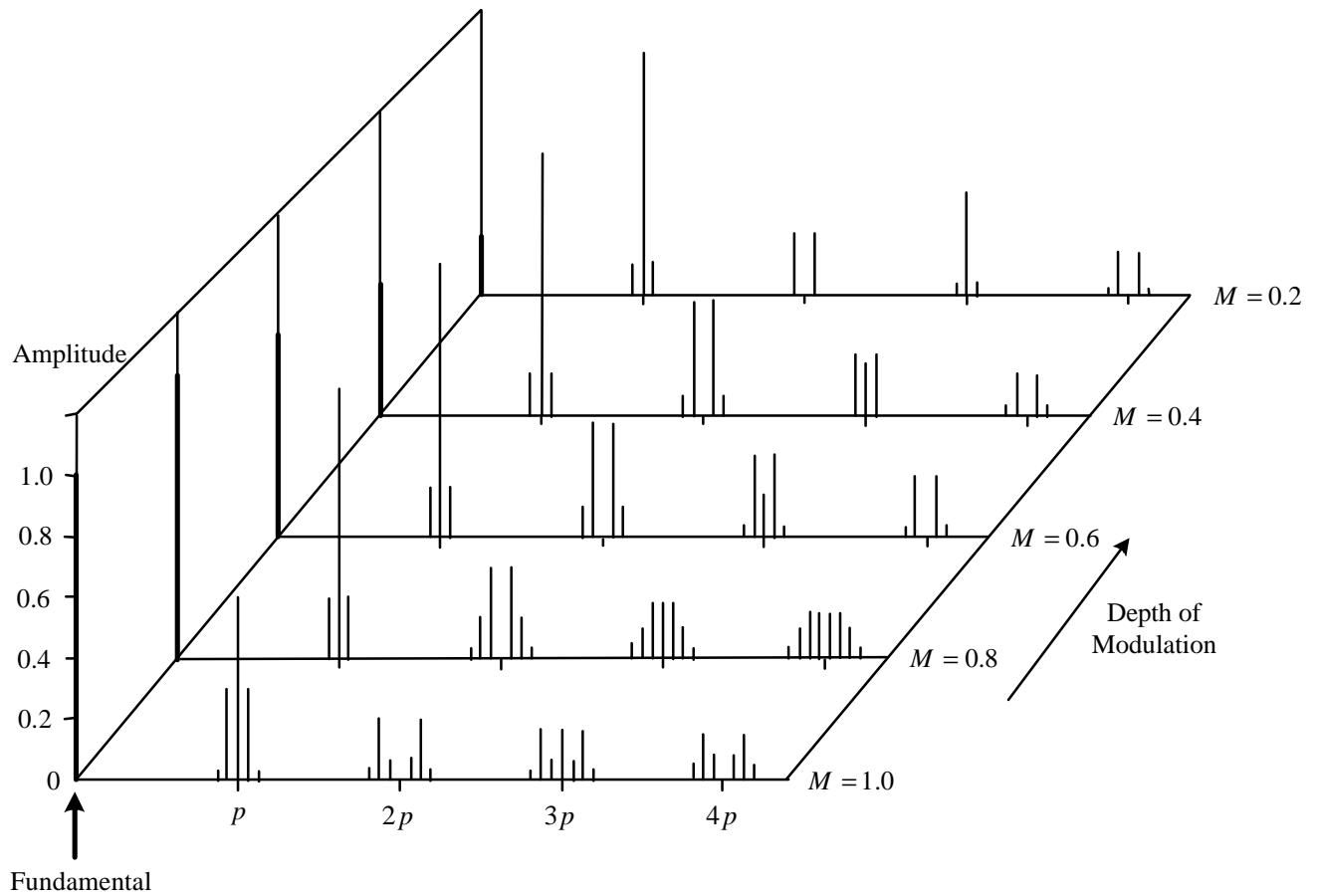
NORMALISED HARMONIC AMPLITUDES FOR
SINUSOIDAL PULSE-WITDH MODULATION

PWM spectra observations

- The amplitude of the fundamental decreases or increases linearly in proportion to the depth of modulation (modulation index). The relation ship is given as: $V_1 = M_I V_{in}$

- The harmonics appear in "clusters" with main components at frequencies of :
  $f = kp\,(f_m);$ $k=1,2,3....$
  where f_m is the frequency of the modulation (sine) waveform. This also equal to the multiple of the carrier frequencies. There also exist "side-bands" around the main harmonic frequencies.

- The amplitude of the harmonic changes with M_I. Its incidence (location on spectra) is not.

- When $p>10$, or so, the harmonics can be normalised as shown in the Figure. For lower values of p, the side-bands clusters overlap, and the normalised results no longer apply.

Three-phase harmonics: "Effect of odd triplens"

- For three-phase inverters, there is significant advantage if p is chosen to be:

  - **odd and multiple of three (triplens)** (e.g. 3,9,15,21, 27..)
  - the waveform and harmonics and shown on the next two slides. Notice the difference?

- By observing the waveform, it can be seen that with odd p, the line voltage shape looks more "sinusoidal".

- The even harmonics are all absent in the phase voltage (pole switching waveform). This is due to the p chosen to be odd.

Spectra observations

- Note the absence of harmonics no. 21, 63 in the inverter line voltage. This is due to p which is multiple of three.

- In overall, the spectra of the line voltage is more "clean". This implies that the THD is less and the line voltage is more sinusoidal.

- It is important to recall that it is the line voltage that is of the most interest.

- Also can be noted from the spectra that the phase voltage amplitude is 0.8 (normalised). This is because the modulation index is 0.8. The line voltage amplitude is square root three of phase voltage due to the three-phase relationship.
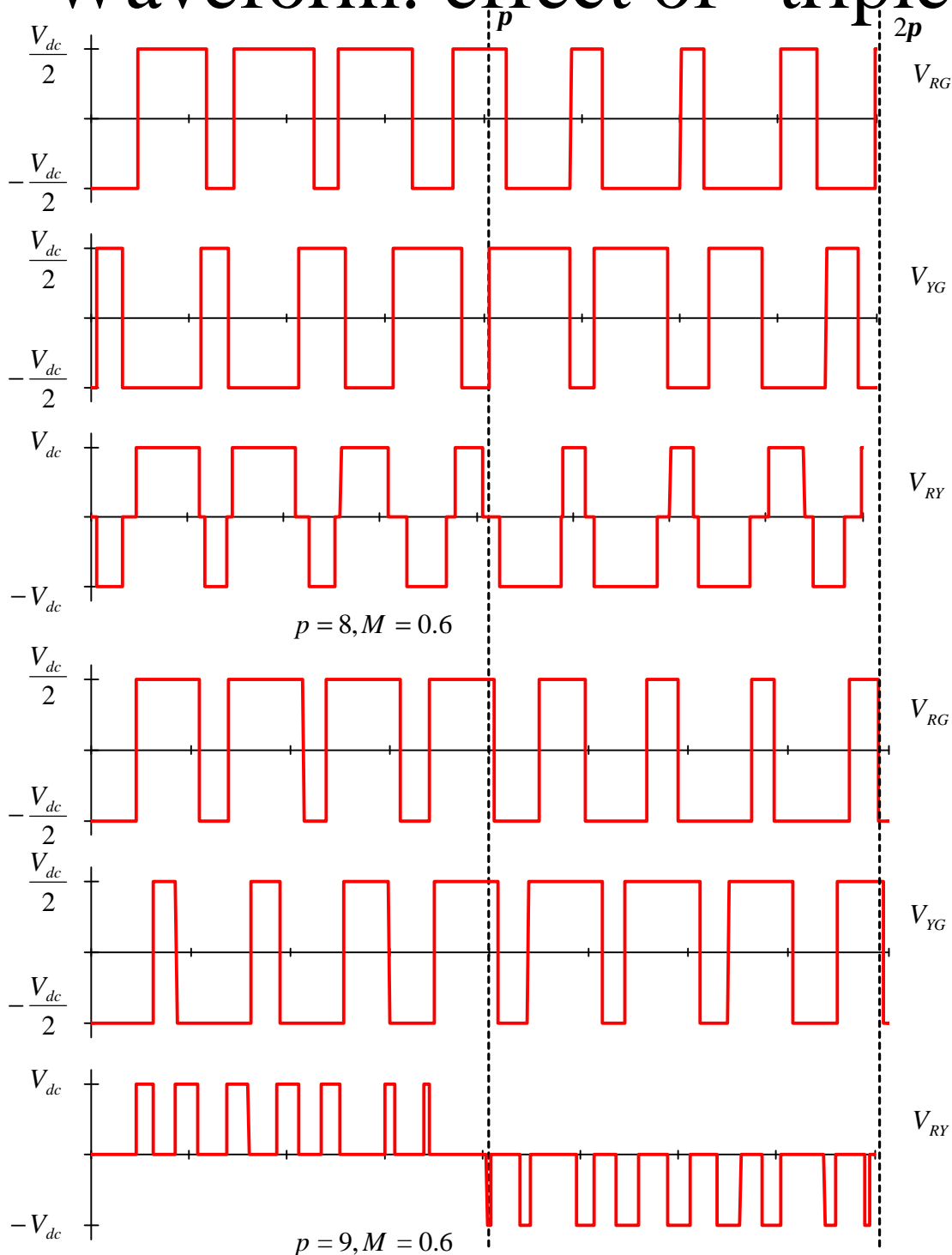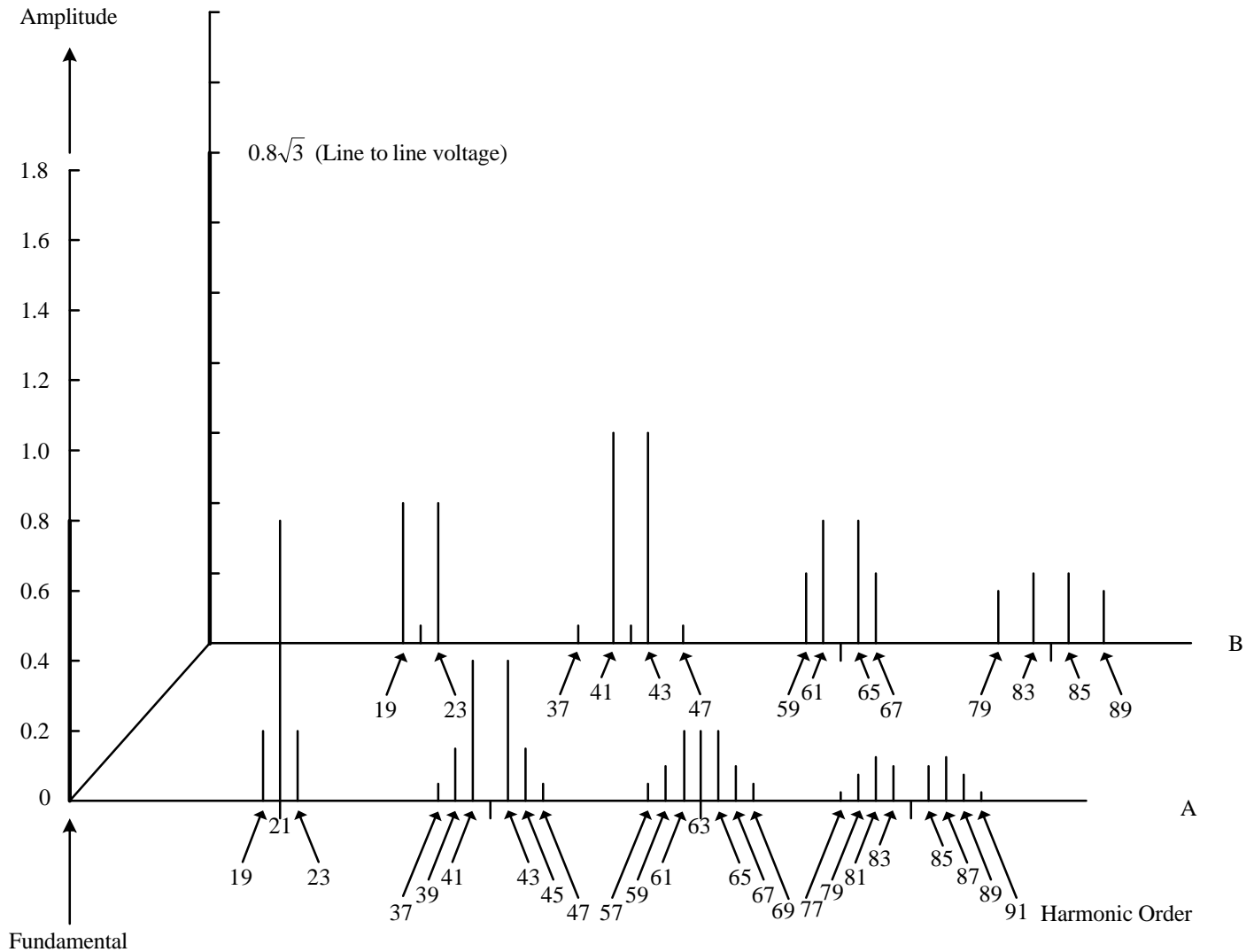
Waveform: effect of "triplens"

$p = 8, M = 0.6$

$p = 9, M = 0.6$

ILLUSTRATION OF BENEFITS OF USING A FREQUENCY RATIO
THAT IS A MULTIPLE OF THREE IN A THREE PHASE INVERTER

Harmonics: effect of "triplens"

COMPARISON OF INVERTER PHASE VOLTAGE (A) & INVERTER LINE VOLTAGE
(B) HARMONIC (P=21, M=0.8)

Comments on PWM scheme

- It is desirable to push p to as large as possible.

- The main impetus for that when p is high, then the harmonics will be at higher frequencies because frequencies of harmonics are related to: $f = kp(f_m)$, where f_m is the frequency of the modulating signal.

- Although the voltage THD improvement is not significant, but the current THD will improve greatly because the load normally has some current filtering effect.

- In any case, if a low pass filter is to be fitted at the inverter output to improve the voltage THD, higher harmonic frequencies is desirable because it makes smaller filter component.