Credit Hours:									
	CS-114 Introduction to Programming 3+1								
Pre Requisite	None								
Instructor (s):	Engr. Hammad Shaukat								
Lab Engineer:	Engr. Shahwar Ali								
Compulsory/Elective: If Elective: Depth Core/ Breadth Core:	Compulsory								
Course Schedule:	Lecture: 3 hours/week								
	Lab:								
	Office hours:								
Course Assessment:	Assignments:	3							
	Quizzes:	3							
	Course project:	1							
	Lab work:								
	Exams:	Mid-semester and Final							
Grading Policy:	Quizzes:								
	Assignments/Course project:	10%							
	Lab work:	20%							
	Mid-Semester:		20%						
	End-Semester:	40%							
Text Book: Reference Book(s):	Computer Fundamentals By Peter Norton 7 th Edition Object Oriented Programming in C++ by Robert Lafore Aikman series C++ Using Information Technology by Brian Williams and Stacey Sawyer, McGraw-Hill, Latest Edition. Computer Organization and Architecture: Designing for Performance by William Stallings, Latest								
	Prentice Hall.			t Edition					
Course Objective:	Prentice Hall. To acquaint the students with the structure,	Designing for Performance by William Stal	llings, Lates						
Course Learning Outcome	Prentice Hall. To acquaint the students with the structure, CLO St	Designing for Performance by William Stal	llings, Lates computers. PLO	Bloom					
Course Learning Outcome CLO-1: CLO-2:	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware.	llings, Lates computers. PLO PLO-11 PLO-2	Bloom C1 C2					
Course Learning Outcome CLO-1:	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware.	llings, Lates computers. PLO PLO-11	Bloom C1					
Course Learning Outcome CLO-1: CLO-2: CLO-3:	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware. grams in different programming languages	llings, Lates computers. PLO PLO-11 PLO-2	Bloom C1 C2 C3					
Course Learning Outcome CLO-1: CLO-2: CLO-3: Topics covered in the	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof An ability to write, debug and execute prog	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware. grams in different programming languages	llings, Lates computers. PLO PLO-11 PLO-2 PLO-3	Bloom C1 C2 C3					
Course Learning Outcome CLO-1: CLO-2: CLO-3: Topics covered in the course and level of	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof An ability to write, debug and execute prog Introducing Computer Systems, H Computer Input and output Device	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware. grams in different programming languages	llings, Lates computers. PLO PLO-11 PLO-2 PLO-3 6 ho	Bloom C1 C2 C3 Durs					
Course Learning Outcome CLO-1: CLO-2: CLO-3: Topics covered in the course and level of	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof An ability to write, debug and execute prog Introducing Computer Systems, H Computer Input and output Device Computer CPU and Processors.	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware. grams in different programming languages lardware and Software es	Ilings, Lates computers. PLO-11 PLO-2 PLO-3 6 ho 6 ho	Bloom C1 C2 C3 Durs Durs Durs					
Course Learning Outcome CLO-1: CLO-2: CLO-3: Topics covered in the course and level of	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof An ability to write, debug and execute prog Introducing Computer Systems, H Computer Input and output Device Computer CPU and Processors. Memory and Storage Devices and	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware. grams in different programming languages lardware and Software es their Construction	llings, Lates computers. PLO-11 PLO-2 PLO-3 6 hc 6 hc 3 hc	Bloom C1 C2 C3 Durs Durs Durs Durs					
Course Learning Outcome CLO-1: CLO-2: CLO-3: Topics covered in the course and level of	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof An ability to write, debug and execute prog Introducing Computer Systems, H Computer Input and output Devices Computer CPU and Processors. Memory and Storage Devices and Transforming Data into Information	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware. grams in different programming languages lardware and Software es their Construction on	Illings, Lates computers. PLO-11 PLO-2 PLO-3 6 hc 6 hc 3 hc 3 hc 6 hc	Bloom C1 C2 C3 Ours ours ours ours ours ours					
Course Learning Outcome CLO-1: CLO-2: CLO-3: Topics covered in the course and level of	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof An ability to write, debug and execute prog Introducing Computer Systems, H Computer Input and output Device Computer CPU and Processors. Memory and Storage Devices and Transforming Data into Informatic programming languages, compilat	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware. grams in different programming languages lardware and Software es their Construction on tion and interpretation, problem specification	llings, Lates computers. PLO PLO-11 PLO-2 PLO-3 6 hc 6 hc 3 hc 6 hc 6 hc 6 hc 6 hc	Bloom C1 C2 C3 Ours Ours Ours Ours Ours Ours					
Course Learning Outcome CLO-1: CLO-2: CLO-3: Topics covered in the course and level of	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof An ability to write, debug and execute prog Introducing Computer Systems, H Computer Input and output Device Computer CPU and Processors. Memory and Storage Devices and Transforming Data into Informatic programming languages, compilat algorithms, flow chart, pseudo cod	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware. grams in different programming languages lardware and Software es their Construction on tion and interpretation, problem specification de, basic programming techniques	llings, Lates computers. PLO-11 PLO-2 PLO-3 6 hc 6 hc 3 hc 6 hc 6 hc 6 hc 6 hc 6 hc 6 hc	Bloom C1 C2 C3 Ours Ours Ours Ours Ours Ours Ours					
Course Learning Outcome CLO-1: CLO-2: CLO-3: Topics covered in the course and level of	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof An ability to write, debug and execute prog Introducing Computer Systems, H Computer Input and output Device Computer CPU and Processors. Memory and Storage Devices and Transforming Data into Informatio programming languages, compilat algorithms, flow chart, pseudo coo data types and declaration, header	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware. grams in different programming languages lardware and Software es their Construction on tion and interpretation, problem specification de, basic programming techniques file and linkage	llings, Lates computers. PLO PLO-11 PLO-2 PLO-3 6 hc 6 hc 6 hc 6 hc 6 hc 6 hc 6 hc 6 hc	Bloom C1 C2 C3 Ours Ours Ours Ours Ours Ours Ours Ours					
Course Learning Outcome CLO-1: CLO-2:	Prentice Hall. To acquaint the students with the structure, CLO St History and introduction to Computer Syste Enable students to understand working and peripherals, Storage media and types of sof An ability to write, debug and execute prog Introducing Computer Systems, H Computer Input and output Device Computer CPU and Processors. Memory and Storage Devices and Transforming Data into Informatic programming languages, compilat algorithms, flow chart, pseudo coo data types and declaration, header variables and constants, arrays, in structures	Designing for Performance by William Stal operation, programming, and applications of atement ems construction of various computer ftware. grams in different programming languages lardware and Software es their Construction on tion and interpretation, problem specification de, basic programming techniques	llings, Lates computers. PLO-11 PLO-2 PLO-3 6 hc 6 hc 3 hc 6 hc 6 hc 6 hc 6 hc 6 hc 6 hc	Bloom C1 C2 C3 Durs Durs Durs Durs Durs Durs Durs Durs					

	• Social impact of computer age, computers in office, industry and education.							ion.	6 hours						
Program learning Detailed Contents									PLO	CLO					
outcome	tcomes and how they History and importance of Computer Systems and their Uses.								PLO-11	CLO-1					
are covered by specific Looking Inside the Computer Systems, Hardware, Software, Data and User.									PLO-1	CLO-2					
Instruction Processing Cycle.									PLO-2	CLO-2					
Using Input and Output Devices such as Keyboard, Mouse, Monitors and its types, and Printers								es, and	PLO-1	CLO-2					
	Transformation of Data into Information and Modern CPU.									PLO-2	CLO-2				
Storage Devices, their use, Construction and Managing files.									PLO-1	CLO-2					
	Introduction to C++ Programming, Preprocessor Directives, header Files, Tokens, Variables, Data Types, Keywords, Identifiers and Operators.								8,	PLO-1	CLO-3				
			Basic		nd outp						ilators, Declar	ation and		PLO-1	CLO-3
Programs in C++ with manipulators										PLO-2	CLO-3				
Conditional Statements and Switch Statements.										PLO-1	CLO-3				
if, if-else, nested if, nested if-else and nested switch statements in C++ programming										PLO-2	CLO-3				
Loops and its Types, do, while, do-while loop and for loop.										PLO-1	CLO-3				
C++ programming problems related to Loops and its types.									PLO-2	CLO-3					
Introduction to uni-directional and multidimensional Arrays								PLO-1	CLO-3						
Mapping	g of CLO	s with Pl	LOs and E	Bloom's	s Taxon	omv	Cognitiv	e Lev	els:						1
PLO	1	2	3	4	5		6		7	8	9	10	1	1	12
CLO-1 CLO-2	C1 C2	C1 C2													
CLO-2 CLO-3 CLO-4	02	C2													
CLU-4															
					1		1	1		1	1		1		
Manning	of CLO	s with A	ssessment	Metho	der										
			CLO-			CLO	-2		CLO	3	CLO-	4			
CLOs/Assessment						CLO-2				5					

CLOs/Assessment	CLO-1	CLO-2	CLO-3	CLO-4	
Assignments:	\checkmark		\checkmark		
Quizzes:		\checkmark			
Course project:			\checkmark		
Lab work:		\checkmark			
Mid-Semester:					
End-Semester:	\checkmark	\checkmark	\checkmark		