Course Number and	EE-315 Communication Electronics							
Title:								
Credit Hours:	3+1							
Pre Requisite	Electronic Devices and Circuits							
Instructor (s):	Dr. Shabbir Majeed							
Lab Engineer:	Engr. M.Waseem							
Compulsory/Elective:	Compulsory							
If Elective:Depth	Engineering Foundation							
Core/Breadth Core:								
Course Schedule:	Lecture: 3 hours/week							
	Lab: 3 hours/week							
	Office hours:	2 hours/week						
Course Assessment:	Assignments:	4						
	Quizzes:	4						
	Course project:	1						
	Lab work:	13 experiments						
	Exams:	Mid-semester and Final						
		1						
Grading Policy:	Ouizzes:	20%						
	Assignments							
	Lab work+ Course project::	20%						
	Mid-Semester:	20%						
	End-Semester:	40%						
Text Book:	Louis E. Frenzel Jr., "Principles of Electronic Communication Systems."							
	Fourth Edition, 2015, McGraw-Hill Education USA							
	Thomas Floyd, "Electronics Fundamentals: Circuits, Devices, and							
	Applications," Sixth Edition, 2004. Prentice Hall, USA.							
Reference Book(s):	Albert Malvino and David J Bates, "Electronic Principles," Seventh Edition.							
	2007, Mcgraw-Hill, ISBN: 9780070634244.							
Course Objective:	To introduce the concept of the use of electronic circuits in communication							
Course Learning	CLO Statement		PL	0	Bloom			
Outcome	CEO Statement							
CLO-1:	To describe the basic operation of	PL	0-1	C2				
	amplifiers used in communication electronics and							
	signal frequency effects on amplifier Gain and Phase							
	shift.							
CLO-2:	Knowledge about different typ	D-2 C1						
	mixers, and filters used in communication circuits.							
Topics covered in the	Introduction to electronic commun		6 hours					
course and level of	Study of Different Base Band and RF Power Amplifiers				9 hours			
coverage:	Frequency Response of Amplifiers				9 hours			
	Introduction to oscillator circuits and their types				9 hours			

	Introduction to Mixer Circuits						6 hours		
	Introduction to Filter Circuits					9 hours			
Program learning	Detailed Contents				CLO		PLO		
outcomes and how they	BJT and FET	Transi	istor as	Power	r Amplifie	ers, large-	CLO	D-1	PLO-2
are covered by specific	signal analysis, gain and Power Efficeicny								
course outcomes:	calculation of	f single-	-stage a	nd mu	ltistage an	nplifier.			
	Class A am	plifier,	class	B an	nplifier, c	class AB	CLO-1		PLO-2
	amplifier, cla	lss C ar	nplifier	, push-	-pull ampl	ifier, and			
	complementary symmetry amplifier.								
	Frequency Response of single-stage and multistage					CLO-1		PLO-2	
	BJT and FET amplifier circuits, Critical Frequencies								
	and Roll off rates, Miller's Effects, Bode Plots								
	Feedback Oscillator Principles, Oscillators with RC					CLO-2		PLO-1	
	and LC fe	and LC feedback circuits, Crystal Controlled							
	Oscillator circuits, Relaxation Oscillators, Voltage								
	Controlled Oscillators						CL O O		
	Introduction to Active and Passive Low Pass, High					CLO-2		PLO-1	
	Pass, Band Pass and Band Stop filter circuits, Gain								
	Introduction to Amplitude modulation and Demodulation, Mixer as a Linear Multiplier, Mixer Circuit Basics								
						CLO-2		PLO-1	
Mapping of CLOs with P	LOs and Bloon	n's Tax	onomy	Cognit	tive Level	s:			
PLO 1 2	3 4	5	6	7	8	9	10	11	12
CLO-1 C2									
CLO-2 C1									
Mapping of CLOs with A	ssessment Met	hods:							
CLOs/Assessment CLO-1			CLO-2						
Quizzes: $$			\checkmark						
Mid-Semester:									
End-Semester:				√					