

Department of Electronics Engineering
List of Publications

1. **Yaseer A. Durrani** "Power and Thermal Modeling Approach for Homogeneously Stacked Butterfly Fat Tree Architecture in 3D ICs" Wiley International Journal of Numerical Modeling, Electronic Networks, Devices & Fields, Vol. 31, no. 1, DOI:10.1002/jnm.2330, **2018**, ISSN: 1099-1204, (IF: 0.515)
2. **Yaseer A. Durrani** "Power Macro-Modeling Technique for NoC-based Homogeneous Layered 3D ICs" Wiley International Journal of Numerical Modeling, Electronic Networks, Devices & Fields, Vol. 30, no. 6, DOI:10.1002/jnm.2309, **2017**, ISSN: 1099-1204, (IF: 0.515)
3. **Yaseer A. Durrani** "Power Analysis Approach for NoC-based Homogeneous Stacked 3D ICs" World Scientific Journal of Circuits, Systems and Computers, Vol. 27, no. 2, pp. 1-16, **2018**, Print ISSN: 0218-1266, Online ISSN: 1793-6454, <https://doi.org/10.1142/S0218126618500342> (IF: 0.47)
4. **Yaseer A. Durrani**, T. Riesgo "Power modeling for High Performance network-on-chip architectures" Elsevier Journal of Microprocessors & Microsystems, Vol. 50, pp. 80-89, **2017**, ISSN:0141-9331, DOI:10.1016/j.micpro.2017.03.003, (IF: 0.571)
5. K. Mahmood, A. Rafique, **Yaseer A. Durrani** "Effect of Isothermal Treatment on Ni3Al Coatings Deposited by Air Plasma Spraying System, Journal Archives of Metallurgy and Materials, Vol. 63, no. 1, ISSN: 1733-3490, **2018** (IF: 0.571)
6. **Yaseer A. Durrani**, T. Riesgo "Efficient Power Analysis Approach and its Application to System-on-Chip Design" Elsevier Journal of Microprocessors & Microsystems, Vol. 46, no. 45, part A, pp. 11-20, **2016**, ISSN:0141-9331, DOI:10.1016/j.micpro.2016.09.003, (IF: 0.571) (Oct, 2016)
7. **Yaseer A. Durrani**, T. Riesgo "Power Macro-Modelling Technique and its application to SoC-based Design" Wiley International Journal of Numerical Modeling, Electronic Networks, Devices & Fields, Vol 29, no.6, **2016**, ISSN: 1099-1204, DOI: 10.1002/jnm.2207, (IF: 0.515)
8. **Yaseer A. Durrani**, T. Riesgo, M. I. Khan, T. Mahmood "Power Analysis approach and its application to IP-based SoC design" Emerald Group Publisher, International Journal of Computation and Mathematics in Electrical and Electronic Engineering" Vol. 35, Issue 3, pp. 1218-1236, **2016**, ISSN: 0332-1649. (IF: 0.371)
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10. M. M. Rauf, M. Shahid, **Y. A. Durrani**, A. N. Khan, A. Hussain, R. Akhter "Cladding of Ni-20Cr Coating by Optimizing the CO2 Laser Parameters" Springer Arabian Journal of Science & Engineering, ISSN: 1319-8025, Vol. 41, No. 6, pp. 2353-2362, **2015**, DOI 10.1007/s13369-015-1972-7. (IF: 0.367) (8-Dec. 2015)

11. I. N. Qureshi, M. Shahid, A. N. Khan, **Y. A. Durrani** "Evaluation of Titanium Nitride – Modified Bondcoat System Used in Thermal Barrier Coating in Corrosive Salts Environment at High Temperature" Springer Journal of Thermal Spray Technology, Vol. 24, No. 8, pp. 1520-1528, DOI:10.1007/s11666-015-0344-x, **2015**, ISSN: 1059-9630. (IF: 1.344)
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13. **Yaseer A. Durrani**, T. Riesgo "Power Estimation Technique for DSP Architecture", Elsevier Journal of Digital Signal Processing, Vol. 19, Issue 2, pp.213-219, **2009**, ISSN:1051-2004, (IF:1.871)
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15. **Yaseer A. Durrani** "Fundamentals of Low-Noise in Analog Circuits", Technical Journal, UET, Taxila, Vol. 22, No. 1, pp. 46-51, ISSN: 1813-1786, Jan, **2017**.
16. **Yaseer A. Durrani** "Design of High Performance IIR filter Using Vedic Multiplier Method", Technical Journal, UET, Taxila, Vol. 21, No. 4, pp. 46-51, ISSN: 1813-1786, Oct, **2016**.
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